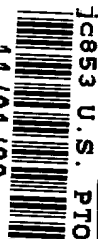


11/21/00



JC853 U.S. PTO

11-22-00

Please type a plus sign (+) inside this box → ☒

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Approved for use through 10/31/2002. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

PTO/SB/05 (08-00)

JC921 U.S. PTO

09/17/567



11/21/00

UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

| | |
|------------------------|---|
| Attorney Docket No. | BFGBP0217USA |
| First Inventor | Paul A. Kohl et al |
| Title | Fabrication of A Semiconductor Device with... |
| Express Mail Label No. | EK347081583US |

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

1. ☒ Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original and a duplicate for fee processing)
2. ☐ Applicant claims small entity status.
See 37 CFR 1.27.
3. ☒ Specification [Total Pages 42]
(preferred arrangement set forth below)
 - Descriptive title of the invention
 - Cross Reference to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to sequence listing, a table, or a computer program listing appendix
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
4. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets 10]
5. Oath or Declaration [Total Pages 1]
 - a. ☐ Newly executed (original or copy)
 - b. ☒ Copy from a prior application (37 CFR 1.63 (d))
(for continuation/divisional with Box 17 completed)
 - i. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
6. ☐ Application Data Sheet. See 37 CFR 1.76

ADDRESS TO: Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

7. ☐ CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)
8. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)
 - a. ☐ Computer Readable Form (CRF)
 - b. Specification Sequence Listing on:
 - i. ☐ CD-ROM or CD-R (2 copies); or
 - ii. ☐ paper
 - c. ☐ Statements verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

9. ☐ Assignment Papers (cover sheet & document(s))
10. ☐ 37 CFR 3.73(b) Statement (when there is an assignee) ☐ Power of Attorney
11. ☐ English Translation Document (if applicable)
12. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
13. ☒ Preliminary Amendment
14. ☒ Return Receipt Postcard (MPEP 503) (Should be specifically itemized)
15. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)
16. ☒ Other: Certificate of Mailing

17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76:

☐ Continuation ☒ Divisional ☐ Continuation-in-part (CIP) of prior application No. 09,009,952
Prior application information. Examiner C. Everhart Group / Art Unit. 2825

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

18. CORRESPONDENCE ADDRESS

☐ Customer Number or Bar Code Label

(Insert Customer No. or Attach bar code label here)

or ☐ Correspondence address below

| | | | | | |
|---------|--|-----------|---------------|----------|---------------|
| Name | Joseph J. Crimaldi | | | | |
| Address | Renner, Otto, Boisselle, & Sklar, L.L.P. 1621 Euclid Avenue, 19th Floor | | | | |
| City | Cleveland | State | OH | Zip Code | 44115 |
| Country | USA | Telephone | (216)621-1113 | Fax | (216)621-6165 |

| | | | |
|-------------------|--------------------|-----------------------------------|---------------|
| Name (Print/Type) | Joseph J. Crimaldi | Registration No. (Attorney/Agent) | 41,690 |
| Signature | | | Date 11/21/00 |

Burden Hour Statement: This form is estimated to take 2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

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FEE TRANSMITTAL for FY 2001

Patent fees are subject to annual revision.

TOTAL AMOUNT OF PAYMENT (\$) 710.00

Complete if Known

Application Number
Filing Date
First Named Inventor Paul A. Kohl et al
Examiner Name C. Everhart
Group Art Unit 2825
Attorney Docket No. BFGBP0217USA

09/11/00
11/21/00

METHOD OF PAYMENT

1. ☒ The Commissioner is hereby authorized to charge indicated fees and credit any overpayments to:
- Deposit Account Number 18-0988
Deposit Account Name Renner, Otto, Boisselle, & Sklar
- ☒ Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17
☐ Applicant claims small entity status See 37 CFR 1.27
2. ☒ Payment Enclosed:
☒ Check ☐ Credit card ☐ Money Order ☐ Other

FEE CALCULATION

1. BASIC FILING FEE

| Large Entity Fee Code (\$) | Small Entity Fee Code (\$) | Fee Description | Fee Paid |
|----------------------------|----------------------------|------------------------|----------|
| 101 710 | 201 355 | Utility filing fee | 710 |
| 106 320 | 206 160 | Design filing fee | |
| 107 490 | 207 245 | Plant filing fee | |
| 108 710 | 208 355 | Reissue filing fee | |
| 114 150 | 214 75 | Provisional filing fee | |

SUBTOTAL (1) (\$) 710

2. EXTRA CLAIM FEES

Total Claims 15 -20** = 0 X Fee from below = 0
Independent Claims 2 -3** = 0 X Fee Paid = 0
Multiple Dependent

| Large Entity Fee Code (\$) | Small Entity Fee Code (\$) | Fee Description |
|----------------------------|----------------------------|--|
| 103 18 | 203 9 | Claims in excess of 20 |
| 102 80 | 202 40 | Independent claims in excess of 3 |
| 104 270 | 204 135 | Multiple dependent claim, if not paid |
| 109 80 | 209 40 | ** Reissue independent claims over original patent |
| 110 18 | 210 9 | ** Reissue claims in excess of 20 and over original patent |

SUBTOTAL (2) (\$) 0

**for number previously paid, if greater, For Reissues, see above

FEE CALCULATION (continued)

3. ADDITIONAL FEES

| Large Entity Fee Code (\$) | Small Entity Fee Code (\$) | Fee Description | Fee Paid |
|----------------------------|----------------------------|--|----------|
| 105 130 | 205 65 | Surcharge - late filing fee or oath | |
| 127 50 | 227 25 | Surcharge - late provisional filing fee or cover sheet | |
| 139 130 | 139 130 | Non-English specification | |
| 147 2,520 | 147 2,520 | For filing a request for ex parte reexamination | |
| 112 920* | 112 920* | Requesting publication of SIR prior to Examiner action | |
| 113 1,840* | 113 1,840* | Requesting publication of SIR after Examiner action | |
| 115 110 | 215 55 | Extension for reply within first month | |
| 116 390 | 216 195 | Extension for reply within second month | |
| 117 890 | 217 445 | Extension for reply within third month | |
| 118 1,390 | 218 695 | Extension for reply within fourth month | |
| 128 1,890 | 228 945 | Extension for reply within fifth month | |
| 119 310 | 219 155 | Notice of Appeal | |
| 120 310 | 220 155 | Filing a brief in support of an appeal | |
| 121 270 | 221 135 | Request for oral hearing | |
| 138 1,510 | 138 1,510 | Petition to institute a public use proceeding | |
| 140 110 | 240 55 | Petition to revive - unavoidable | |
| 141 1,240 | 241 620 | Petition to revive - unintentional | |
| 142 1,240 | 242 620 | Utility issue fee (or reissue) | |
| 143 440 | 243 220 | Design issue fee | |
| 144 600 | 244 300 | Plant issue fee | |
| 122 130 | 122 130 | Petitions to the Commissioner | |
| 123 50 | 123 50 | Petitions related to provisional applications | |
| 126 240 | 126 240 | Submission of Information Disclosure Stmt | |
| 581 40 | 581 40 | Recording each patent assignment per property (times number of properties) | |
| 146 710 | 246 355 | Filing a submission after final rejection (37 CFR § 1.129(a)) | |
| 149 710 | 249 355 | For each additional invention to be examined (37 CFR § 1.129(b)) | |
| 179 710 | 279 355 | Request for Continued Examination (RCE) | |
| 169 900 | 169 900 | Request for expedited examination of a design application | |

Other fee (specify)

*Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$)

SUBMITTED BY

| Name (Print/Type) | Registration No (Attorney/Agent) | Telephone |
|--------------------|----------------------------------|---------------|
| Joseph J. Crimaldi | 41,690 | (216)621-1113 |
| Signature | Date | 11/21/00 |

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[illegible]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | | |
|------------------------------|--|---|--------------------------------|
| In re patent application of: | | : | |
| Applicant: | Paul A. Kohl et al. | : | Expected Art Unit: 2825 |
| Serial No.: | Unknown | : | Expected Examiner: C. Everhart |
| Filing Date: | Herewith | : | |
| For: | FABRICATION OF A SEMICONDUCTOR DEVICE WITH AIR GAPS FOR ULTRA-LOW CAPACITANCE INTERCONNECTIONS | | |

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination of the above patent application please enter the following Preliminary Amendment and remarks.

AMENDMENTS

In The Specification:

Please amend the specification as follows:

Page 1, lines 5-7, please replace "This application is a continuation of U.S. Provisional Application No. 60/035,848 filed on January 21, 1997 and entitled "Air-gaps for Electrical Interconnections."" with --This application is a continuation of U.S. Serial No. 09/009,952 which is a continuation of Provisional Application No. 60/035,848 filed on January 21, 1997 and entitled "Air-gaps for Electrical Interconnections".--

In The Claims:

Please amend the claims in the below-indicated manner:

Please cancel claims 1-42, without prejudice or disclaimer

Please add the following new claims:

--43. (Added) A semiconductor device containing at least one air gap made in accordance with a method comprising the steps of:

(A) forming a patterned layer of sacrificial material on a substrate corresponding to a pattern of one or more gaps to be formed in the semiconductor structure;

(B) depositing a second material on the substrate within regions bordered by the sacrificial material with the second material being formed with a height less than the height of the adjacent sacrificial material;

(C) forming an overcoat layer of material overlying the patterned layer of sacrificial material and second material in the regions bordered by the sacrificial material;

(D) causing the sacrificial material to decompose into one or more gaseous decomposition products; and

(E) removing at least one of the one or more gaseous decomposition products by passage through the overcoat layer so that one or more air gaps are formed within the semiconductor structure,

whereby the height of the one or more air gaps exceeds the height of the one or more areas of second material.

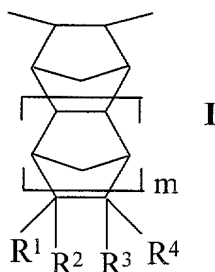
44. (Added) The semiconductor device as set forth in claim 43, wherein step (B) includes using a conductive material to form conductive leads on opposite sides of portions of the sacrificial material.

45. (Added) The semiconductor device as set forth in claim 43, wherein the sacrificial material is a cyclic olefin.

46. (Added) The semiconductor device as set forth in claim 45, wherein the cyclic olefin is a dicyclic olefin.

47. (Added) The semiconductor device as set forth in claim 43, wherein the sacrificial material is a norbornene-type polymer.

48. (Added) The semiconductor device as set forth in claim 47, wherein the norbornene-type polymer comprises repeat units of the general formula:

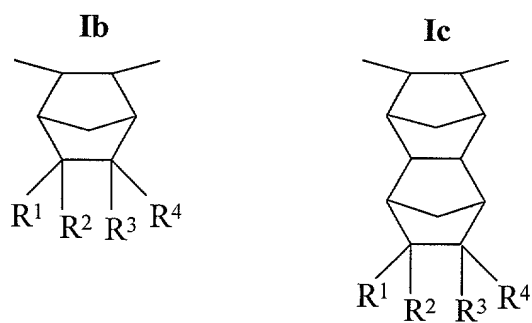


wherein R^1 and R^4 independently represent hydrogen or linear or branched (C_1 to C_{20}) alkyl; R^2 and R^3 independently represent hydrogen, linear or branched (C_1 to C_{20}) alkyl or the groups:



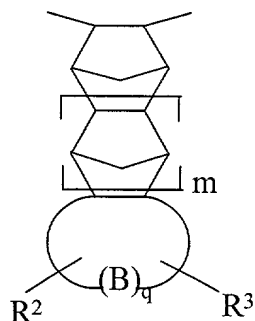
R^9 independently is hydrogen, methyl, or ethyl; R^{10} , R^{11} , and R^{12} independently represent linear or branched (C_1 to C_{20}) alkyl, linear or branched (C_1 to C_{20}) alkoxy, linear or branched (C_1 to C_{20}) alkyl carbonyloxy, and substituted or unsubstituted (C_6 to C_{20}) aryloxy; m is a number from 0 to 4; and n is a number from 0 to 5; and at least one of substituents R^2 and R^3 is selected from the silyl group represented by the formula set forth under **la**.

49. (Added) The semiconductor device as set forth in claim 48, wherein in Formula I above, m is preferably 0 or 1 as represented by structures **Ib** and **Ic**, respectively:



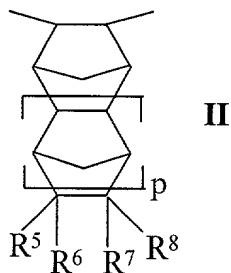
wherein R^1 to R^4 are as previously defined and at least one of R^2 and R^3 is a silyl substituent represented by **la**.

50. (Added) The semiconductor device as set forth in claim 48, wherein R^1 and R^4 taken together with the two ring carbon atoms to which they are attached comprise a repeat unit of the following structure:



wherein B is a methylene group, q is a number from 2 to 6, and R² and R³ are as defined above.

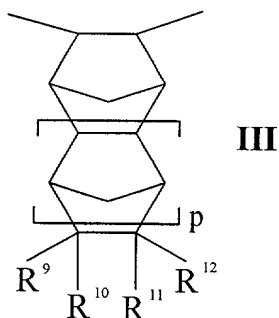
51. (Added) The semiconductor device as set forth in claim 48, wherein the norbornene-type polymer further comprises hydrocarbyl substituted polycyclic repeating units selected from units represented by Formula II below:



wherein R⁵, R⁶, R⁷, and R⁸ independently represent hydrogen, linear and branched (C₁ to C₂₀) alkyl, hydrocarbyl substituted and unsubstituted (C₅ to C₁₂) cycloalkyl, hydrocarbyl substituted and unsubstituted (C₆ to C₄₀) aryl, hydrocarbyl substituted and unsubstituted (C₇ to C₁₅) aralkyl, (C₃ to C₂₀) alkynyl, linear and branched (C₃ to C₂₀) alkenyl, or vinyl; any of R⁵ and R⁶ or R⁷ and R⁸ can be taken together to form a (C₁ to C₁₀) alkylidenyl group, R⁵ and R⁸ when taken with the two ring carbon atoms to which they are attached can represent saturated and unsaturated cyclic groups containing 4

to 12 carbon atoms or an aromatic ring containing 6 to 17 carbon atoms; and p is 0, 1, 2, 3, or 4.

52. (Added) The semiconductor device as set forth in claim 47, wherein the norbornene-type polymer comprises repeating units represented by Formula III below:



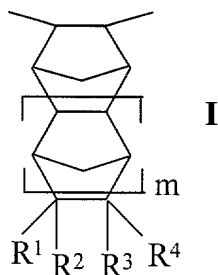
wherein R^9 to R^{12} independently represent a polar substituent selected from the group:

$-(A)_n-C(O)OR''$, $-(A)_n-OR''$, $-(A)_n-OC(O)R''$, $-(A)_n-OC(O)OR''$,
 $-(A)_n-C(O)R''$, $-(A)_n-OC(O)C(O)OR''$, $-(A)_n-O-A'-C(O)OR''$,
 $-(A)_n-OC(O)-A'-C(O)OR''$, $-(A)_n-C(O)O-A'-C(O)OR''$, $-(A)_n-C(O)-A'-OR''$,
 $-(A)_n-C(O)O-A'-OC(O)OR''$, $-(A)_n-C(O)O-A'-O-A'-C(O)OR''$,
 $-(A)_n-C(O)O-A'-OC(O)C(O)OR''$, $-(A)_n-C(R'')_2CH(R'')(C(O)OR'')$, and

$-(A)_n-C(R'')_2CH(C(O)OR'')_2$; the moieties A and A' independently represent a divalent bridging or spacer radical selected from divalent hydrocarbon radicals, divalent cyclic hydrocarbon radicals, divalent oxygen containing radicals, and divalent cyclic ethers and cyclic diethers; and n is an integer 0 or 1.

53. (Added) The semiconductor device as set forth in claim 47, wherein the norbornene-type polymer comprises copolymers comprising a combination of repeating units represented by Formulae I and II, Formulae I and III, Formulae II and III or Formulae I, II and III, where

Formula I is:

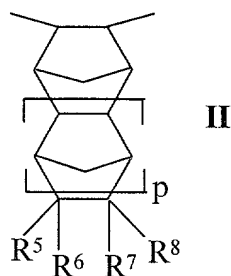


wherein R¹ and R⁴ independently represent hydrogen or linear or branched (C₁ to C₂₀) alkyl; R² and R³ independently represent hydrogen, linear or branched (C₁ to C₂₀) alkyl or the groups:



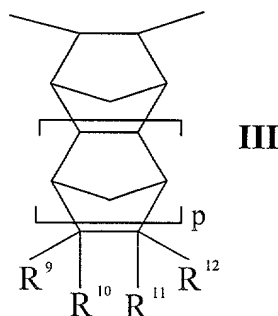
R⁹ independently is hydrogen, methyl, or ethyl; R¹⁰, R¹¹, and R¹² independently represent linear or branched (C₁ to C₂₀) alkyl, linear or branched (C₁ to C₂₀) alkoxy, linear or branched (C₁ to C₂₀) alkyl carbonyloxy, and substituted or unsubstituted (C₆ to C₂₀) aryloxy; m is a number from 0 to 4; and n is a number from 0 to 5; and at least one of substituents R² and R³ is selected from the silyl group represented by the formula set forth under Ia;

Formula II is



wherein R^5 , R^6 , R^7 , and R^8 independently represent hydrogen, linear and branched (C_1 to C_{20}) alkyl, hydrocarbyl substituted and unsubstituted (C_5 to C_{12}) cycloalkyl, hydrocarbyl substituted and unsubstituted (C_6 to C_{40}) aryl, hydrocarbyl substituted and unsubstituted (C_7 to C_{15}) aralkyl, (C_3 to C_{20}) alkynyl, linear and branched (C_3 to C_{20}) alkenyl, or vinyl; any of R^5 and R^6 or R^7 and R^8 can be taken together to form a (C_1 to C_{10}) alkylidenyl group, R^5 and R^8 when taken with the two ring carbon atoms to which they are attached can represent saturated and unsaturated cyclic groups containing 4 to 12 carbon atoms or an aromatic ring containing 6 to 17 carbon atoms; and p is 0, 1, 2, 3, or 4; and

Formula III is



wherein R^9 to R^{12} independently represent a polar substituent selected from the group:
 $-(A)_n-C(O)OR''$, $-(A)_n-OR''$, $-(A)_n-OC(O)R''$, $-(A)_n-OC(O)OR''$,
 $-(A)_n-C(O)R''$, $-(A)_n-OC(O)C(O)OR''$, $-(A)_n-O-A'-C(O)OR''$,
 $-(A)_n-OC(O)-A'-C(O)OR''$, $-(A)_n-C(O)O-A'-C(O)OR''$, $-(A)_n-C(O)-A'-OR''$,
 $-(A)_n-C(O)O-A'-OC(O)OR''$, $-(A)_n-C(O)O-A'-O-A'-C(O)OR''$,

$-(A)_n-C(O)O-A'-OC(O)C(O)OR''$, $-(A)_n-C(R'')_2CH(R'')(C(O)OR'')$, and $-(A)_n-C(R'')_2CH(C(O)OR'')_2$; the moieties A and A' independently represent a divalent bridging or spacer radical selected from divalent hydrocarbon radicals, divalent cyclic hydrocarbon radicals, divalent oxygen containing radicals, and divalent cyclic ethers and cyclic diethers; and n is an integer 0 or 1.

54. (Added) The semiconductor device as set forth in claim 47, wherein the repeating units containing silyl functional groups comprise at least 1 mole percent of the polymer.

55. (Added) The semiconductor device as set forth in claim 54, wherein the repeating units containing silyl functional groups comprise at least 5 mole percent of the polymer.

56. (Added) A semiconductor comprising:
a substrate;
a patterned layer of conductive material on the substrate, the patterned layer having one or more air gaps surrounded by the conductive material;
an overcoat layer of a material overlying the patterned layer and the one or more air gaps;

wherein the one or more air gaps have a height which exceeds the height of the relatively adjacent portions of the conductive material.

57. (Added) The semiconductor of claim 56, wherein the overcoat layer is a dielectric layer.--.

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REMARKS

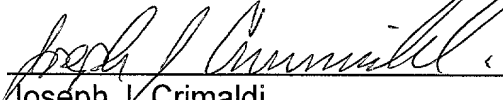
Claims 43-57 are pending in the application upon entry of this amendment. Newly added claims 43-57 are supported by the specification and claims as originally filed in both this application and the parent application (USSN 09/009,952). Entry of the above amendments and favorable examination in view of the same is respectfully requested

Should the Examiner feel that a telephone interview would be helpful to facilitate favorable prosecution of the above-identified application, the Examiner is invited to contact the undersigned at the telephone number provided below.

Should any fees be due as a result of the filing of this response, the Commissioner is hereby authorized to charge the Deposit Account No. 18-0988.

Respectfully submitted,

RENNER, OTTO, BOISSELLE & SKLAR, P.L.L.



Joseph J. Crimaldi
Registration No. 41,690

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1621 Euclid Avenue
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Cleveland, Ohio 44115
(216) 621-1113

C:\Joe\BFGood\P217\P217a\217Aprelimamd.wpd

Title: FABRICATION OF A SEMICONDUCTOR DEVICE WITH AIR GAPS
FOR ULTRA-LOW CAPACITANCE INTERCONNECTIONS

RELATED APPLICATION DATA

5 This application is a continuation of U.S. Provisional Application No.
60/035,848 filed on January 21, 1997 and entitled "Air-gaps for Electrical
Interconnections."

FIELD OF THE INVENTION

The invention herein described relates generally to the fabrication of
10 semiconductor devices and more specifically to such devices that use air gaps
to reduce capacitive coupling between conductors in such devices.

BACKGROUND OF THE INVENTION

As a consequence of the progress made in integrated circuit technology,
the spacing between the metal lines on any given plane of an integrated circuit
15 has become less and less, now extending into the submicrometer range. By
reducing the spacing between conductive members in the integrated circuit, an
increase in capacitive coupling occurs. This increase in capacitive coupling
causes greater crosstalk, higher capacitive losses and increased RC time
constant.

20 In order to reduce capacitive coupling, much effort has been directed
toward developing low dielectric constant (low-K) materials to replace
conventional dielectric materials that are interposed between the metal lines on
a given layer and between layers. Many conventional electronic insulators have
dielectric constants in the 3.5 to 4.2 range. For example, silicon dioxide has a
25 dielectric constant of 4.2 and polyimides typically have dielectric constants from
2.9 to 3.5. Some advanced polymers have dielectric constants in the 2.5 to 3.0
range. Materials in the 1.8 to 2.5 range are also known, but such materials
have had associated therewith severe processing, cost and materials problems.

The lowest possible, or ideal, dielectric constant is 1.0, which is the
30 dielectric constant of a vacuum. Air is almost as good with a dielectric constant
of 1.001. With this recognition of the low dielectric constant of air, attempts
have been made to fabricate semiconductor devices with air gaps between

metal leads to reduce the capacitive coupling between the electrically conducting members. The air gap forming techniques that have been developed have varying degrees of complexity.

U.S. Patent No. 4,987,101 describes a method and structure for providing
5 an insulating electrical space between two lines on a layer of material or between lines on adjacent superposed layers of material. A base member is formed having a plurality of support members extending upwardly from the base member. A removable material is deposited on the base member and around the support members. A cap member of insulating material is then disposed
10 over said support members and the removable material. Access openings are formed in at least one of the base member or the cap member communicating with the removable material. The removable material is removed through the access openings to thereby define a space between the cap member and the base member and between the support members. During this step a partial
15 vacuum (in which some inert gas may be dispersed) may be created in the space vacated by the removable material. The access openings are then filled in so as to provide a sealed space between the cap member and the base member which has a very low dielectric constant.

U.S. Patent No. 5,324,683 describes several techniques for forming air
20 gaps or regions in a semiconductor device. The air regions are formed by either selectively removing a sacrificial spacer or by selectively removing a sacrificial layer. The air regions are sealed, enclosed or isolated by either a selective growth process or by a non-conformal deposition technique. The air regions may be formed under any pressure, gas concentration or processing condition.

25 The techniques disclosed in the aforesaid patents rely on holes or other passageways for effecting removal of the sacrificial material. In U.S. Patent No. 5,461,003, a sacrificial material is removed through a porous dielectric layer. According to this patent, metal leads are formed on a substrate, after which a disposable solid layer is deposited on the metal leads and substrate. The
30 disposable solid layer is then etched back to expose the tops of the metal leads. Then a porous dielectric layer is deposited over the metal leads and disposable

layer. This is followed by removal of the disposable layer which is said to be preferably accomplished by exposing the device to oxygen or oxygen-plasma at a high temperature ($>100^{\circ}\text{C}$) to vaporize, or burn off, the disposable layer. The oxygen moves through the porous dielectric layer to reach and react with the disposable layer and thereby convert it to a gas that moves back out of the porous dielectric layer. Upon removal of the disposable layer, air gaps are left to provide a low dielectric constant. Finally, a non-porous dielectric layer is deposited on top of the porous dielectric layer to seal the porous dielectric layer from moisture, provide improved structural support and thermal conductivity, and passivate the porous dielectric layer. This procedure results in an air gap that does not extend the full height of the adjacent metal leads or lines. The '003 patent discloses a modified method to remedy this and increase the process margin. This modified method involves a further process step wherein an oxide layer is formed on top of the metal leads so that the disposable dielectric layer can extend higher than the metal leads.

It is also noted that the exposure of the device to an oxygen plasma which must diffuse through a porous layer is not only inefficient, it also exposes other elements of the device to a potentially damaging oxygen plasma for an extended period of time. In particular, exposure of oxygen plasma to copper lines can prove deleterious. Copper is becoming an increasingly important metal in semiconductor manufacturing due to its lower resistivity when compared to aluminum.

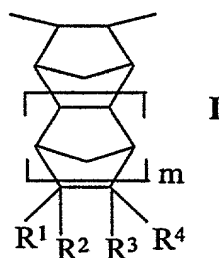
SUMMARY OF THE INVENTION

The present invention provides a method of forming an air gap or gaps within solid structures and specifically semiconductor structures to reduce capacitive coupling between electrical elements such as metal lines. Such method overcomes one or more of the drawbacks associated with the aforesaid prior attempts to reduce capacitive coupling in semiconductor structures such as integrated circuits and packages.

According to one aspect of the invention, a method of forming an air gap in a semiconductor structure comprises the steps of (i) using a norbornene-type

polymer as a sacrificial material to occupy a closed interior volume in a semiconductor structure; (ii) causing the sacrificial material to decompose (preferably self-decompose upon thermal treatment) into one or more gaseous decomposition products; and (iii) removing at least one of the one or more
 5 gaseous decomposition products by passage through at least one solid layer contiguous to the interior volume. The decomposition of the sacrificial material leaves an air gap at the closed interior volume previously occupied by the norbornene-type polymer.

In a preferred embodiment, the solid layer is a dielectric material through
 10 which at least one of the one or more gaseous decomposition products can pass by diffusion under conditions not detrimental to the semiconductor structure. Also, the norbornene-type polymer preferably is of the type herein described, which comprises repeat units of the general formula:



wherein R^1 and R^4 independently represent hydrogen or linear or branched (C_1 to C_{20}) alkyl; R^2 and R^3 independently represent hydrogen, linear or branched (C_1 to C_{20}) alkyl or the groups:



R^9 independently is hydrogen, methyl, or ethyl; R^{10} , R^{11} , and R^{12} independently represent linear or branched (C_1 to C_{20}) alkyl, linear or branched (C_1 to C_{20})
 30 alkoxy, linear or branched (C_1 to C_{20}) alkyl carbonyloxy, and substituted or unsubstituted (C_6 to C_{20}) aryloxy; m is a number from 0 to 4; and n is a number

from 0 to 5; and at least one of substituents R² and R³ is selected from the silyl group represented by the formula set forth under Ia.

More generally, the sacrificial polymers useful in practicing the present invention preferably encompass homopolymers and copolymers containing
5 random repeating units derived from a monomer unit or monomer units represented by Formula I, or homopolymers or copolymers containing random repeating units derived from monomer unit or units represented by the below set forth Formula II, homopolymers or copolymers containing repeating units derived from a monomer unit(s) represented by below set forth Formula III and
10 copolymers comprising a combination of repeating units represented by Formulae I and II, Formulae I and III, Formulae II and III or Formulae I, II and III.

According to another aspect of the invention, a method of forming one or more air gaps in a semiconductor structure comprising the steps of (i) forming a patterned layer of sacrificial material on a substrate corresponding to a pattern
15 of one or more gaps to be formed in the semiconductor structure; (ii) depositing a second material on the substrate within regions bordered by the sacrificial material; (iii) forming an overcoat layer of material overlying the patterned layer of sacrificial material and second material in the regions bordered by the sacrificial material; (iv) causing the sacrificial material to decompose into one or
20 more gaseous decomposition products; and (v) removing at least one of the one or more gaseous decomposition products by passage through the overcoat layer so that one or more air gaps are formed within the semiconductor structure. It will be appreciated by the skilled artisan that the described process is particularly well suited to the copper-damascene process in that the dielectric material is
25 patterned prior to metal deposition.

As is preferred, depositing of the second material includes using a conductive material to form conductive leads on opposite sides of portions of the sacrificial material. Prior to the forming of the overcoat layer, the conductive material may be formed with a height less than the height of the adjacent
30 sacrificial material, such that the resultant air gaps extend beyond to the conductive leads, as may be desired to eliminate fringe effects.

According to a further aspect of the invention, a method of forming an air gap within a structure comprising the steps of (i) using a sacrificial material to occupy a closed interior volume in the structure; (ii) heating the sacrificial material to cause it to decompose into one or more gaseous decomposition products; and (ii) removing at least one of the one or more gaseous decomposition products by passage through at least one solid layer contiguous to the interior volume. As before, the decomposition of the sacrificial material leaves an air gap at the closed interior volume previously occupied by the sacrificial material.

10 The invention also provides structures, and particularly semiconductor structures, produced in accordance with the method of the invention.

The foregoing and other features of the invention are hereinafter fully described and particularly pointed out in the claims, the following description and the annexed drawings setting forth in detail one or more illustrative embodiments of the invention, such being indicative, however, of but one or a few of the various ways in which the principles of the invention may be employed.

BRIEF DESCRIPTION OF THE DRAWINGS

Figs. 1A-1D are diagrammatic cross-sections of a portion of a semiconductor structure, illustrating several steps of a method according to one aspect of the invention.

Figs. 2A-2F are diagrammatic cross-sections of a portion of a semiconductor structure, illustrating several steps of a method according to another aspect of the invention.

Figs. 3A-3F are diagrammatic cross-sections of a portion of a semiconductor structure, illustrating several steps of a method according to further aspect of the invention.

Figs. 3A-3F are diagrammatic cross-sections of a portion of a semiconductor structure, illustrating several steps of a method according to further aspect of the invention.

Figs. 4A-4H are diagrammatic cross-sections of a portion of a semiconductor structure, illustrating several steps of a specific example of a method according to the invention.

Figs. 5A-5J are diagrammatic cross-sections of a portion of a semiconductor structure, illustrating several steps of another specific example of a method according to the invention.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

Referring now in detail to the drawings, the various steps of a method of producing a structure according to one aspect of the invention are diagrammatically illustrated in Figs. 1A-1D. In Figs. 1A and 1B, a patterned layer of sacrificial material 20 is formed on a substrate 22 by any suitable technique. This may be accomplished, for example, by first forming a layer of the sacrificial material on the substrate 22 as depicted in Fig. 1A and then patterning the layer, for example by etching or any other suitable technique, to form the patterned layer of sacrificial material 20 having one or more "hills" formed by the sacrificial material on the substrate with "valleys" formed between any two relatively adjacent hills. A second solid layer of a non-sacrificial material 24 is then formed on the patterned layer in overlying relation to the patterned layer 20 as depicted in Fig. 1C. Then, heat is applied to decompose the sacrificial material into one or more gaseous decomposition products, and one or more of these decomposition products are removed by passing through the second layer 24. This provides an air-gap structure 28 having one or more air gaps 26 in the closed interior space or spaces previously occupied by the sacrificial material 20.

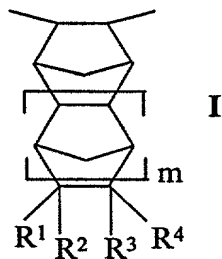
Generally, an air-gap structure is formed by using two dielectric materials, a sacrificial material which decomposes to gaseous products and a permanent material (at least permanent for purposes of forming the interior air gap or gaps) which forms a cover or overcoat through which one or more of the gaseous products can pass. Preferably such passage is by diffusion of one or more of the decomposition products through the overcoat material. More preferably, the decomposition reaction of the sacrificial material is induced solely by high

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temperature although other means may be used. The decomposition temperature should be compatible with the various components of the structure so as not to destroy the integrity thereof aside from the removal of the sacrificial material to form the air gap or gaps. Typically, such temperature should be less than about 500°C for electrical interconnect devices and more preferably less than 450°C. It is contemplated that the decomposition temperature will fall in the range of about 380°C to about 450°C, although materials having decomposition temperatures as low as about 150°C may be beneficial. The sacrificial material, however, should be sufficiently thermally stable so that the permanent material can be processed to a condition where it is electrically and/or mechanically stable.

It should be further noted that any one or more of the herein described layers may be composed of multiple sub-layers, as may desired for different fabricating techniques. For example, the layer 24 in Fig. 1C may be composed of a first sub-layer at the same level as the sacrificial layer and a second sub-layer overlying the first sub-layer and the sacrificial material. Also, the indication that a layer is applied to an underlying layer does is not intended to preclude the presence of an intermediate layer that might be employed, for example, to enable bonding of one layer to another.

The preferred sacrificial material for carrying out the above-described method preferably is selected from the cycloolefin class of compounds and more preferably is a bicycloolefin with a norbornene-type polymer being most preferred. By norbornene-type polymer is meant polycyclic addition homopolymers and copolymers comprising repeating units set forth under Formulae I, II and III below. Copolymers useful in practicing the invention can include repeating units selected from the group comprising and/or consisting of Formulae I, II and III, or combinations thereof. The norbornene-type polymer that is particularly useful as the sacrificial material in the present invention is sold under the Avatrel® trademark by The BFGoodrich Company, Akron, Ohio. The polymer comprises silyl substituted repeating units represented by the structure set forth under Formula I below.



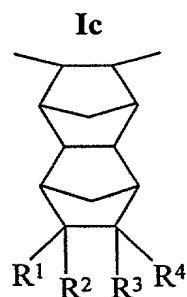
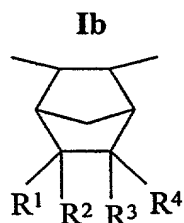
5 wherein R¹ and R⁴ independently represent hydrogen; linear or branched (C₁ to C₂₀) alkyl; R² and R³ independently represent hydrogen, linear or branched (C₁ to C₂₀) alkyl or the group:



15 R⁹ independently is hydrogen, methyl, or ethyl; R¹⁰, R¹¹, and R¹² independently represent linear or branched (C₁ to C₂₀) alkyl, linear or branched (C₁ to C₂₀) alkoxy, linear or branched (C₁ to C₂₀) alkyl carbonyloxy (e.g., acetoxy), and substituted or unsubstituted (C₆ to C₂₀) aryloxy; m is a number from 0 to 4; and n is a number from 0 to 5. In Formula I at least one of substituents R² and R³ must
20 be selected from the silyl group represented by the formula set forth under Ia.

Preferably, at least one of R¹⁰, R¹¹, or R¹² is selected from a linear or branched (C₁ to C₁₀) alkoxy group and R⁹ is hydrogen. More preferably, each of R¹⁰, R¹¹, and R¹² are the same and are selected from methoxy, ethoxy, propoxy, butoxy, and pentoxy. Most preferably, n is 0 and R¹⁰, R¹¹, and R¹² are each
25 ethoxy groups, e.g., R² and/or R³ is most preferably a triethoxysilyl substituent. When n is 0, it is evident that the silyl functionality is connected directly to the polycyclic ring through a silicon-carbon bond wherein the carbon atom of said silicon-carbon bond is supplied by a carbon atom on the polycyclic ring (i.e., a ring carbon atom).

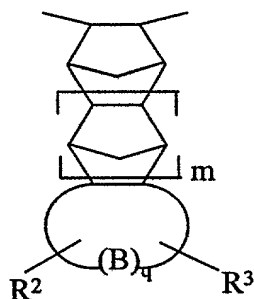
30 In Formula I above, m is preferably 0 or 1 as represented by structures Ib and Ic, respectively, below:



5

wherein R^1 to R^4 are as previously defined and at least one of R^2 and R^3 must be a silyl substituent represented by Ia. Repeating units where m is 0, i.e., repeating units of structure Ib, are especially preferred.

In Formulae I, Ib, and Ic R^1 and R^4 can taken together with the two ring carbon atoms to which they are attached to represent a saturated cyclic group of 4 to 8 carbon atoms. When R^1 and R^4 are taken together to form a saturated cyclic group, the cyclic group is substituted by R^2 and R^3 at least one of which must be a silyl group represented by Ia. Generically such monomers are represented by the following structure:



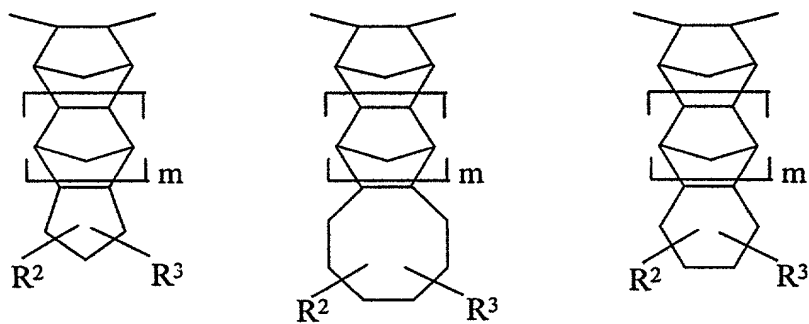
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wherein B is a methylene (i.e., $-\text{CH}_2-$) group and q is a number from 2 to 6. It should be apparent that when the methylene group represented by B contains an R^2 or R^3 substituent, one of the hydrogen atoms of the $-\text{CH}_2-$ group is replaced by the R^2 or R^3 substituent. Representative repeating unit structures are set forth below:

30

5



wherein R^2 , R^3 , and m are as previously defined.

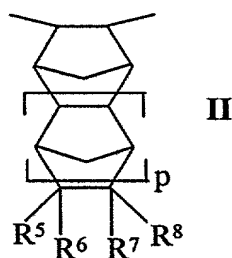
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Illustrative examples of monomers of Formula I include 5-triethoxysilyl-norbornene, 5-trimethylsilyl norbornene, 5-trimethoxysilyl norbornene, 5-methyldimethoxysilyl norbornene, 5-dimethylmethoxy norbornene.

15

In another embodiment of the present invention, the sacrificial polymer comprises hydrocarbyl substituted polycyclic repeating units selected from units represented by Formula II below:

20



25

wherein R^5 , R^6 , R^7 , and R^8 independently represent hydrogen, linear and branched (C_1 to C_{20}) alkyl, hydrocarbyl substituted and unsubstituted (C_5 to C_{12}) cycloalkyl, hydrocarbyl substituted and unsubstituted (C_6 to C_{40}) aryl, hydrocarbyl substituted and unsubstituted (C_7 to C_{15}) aralkyl, (C_3 to C_{20}) alkynyl, linear and branched (C_3 to C_{20}) alkenyl, or vinyl; any of R^5 and R^6 or R^7 and R^8 can be taken together to form a (C_1 to C_{10}) alkylidenyl group, R^5 and R^8 when taken with the two ring carbon atoms to which they are attached can represent saturated and unsaturated cyclic groups containing 4 to 12 carbon atoms or an aromatic ring containing 6 to 17 carbon atoms; and p is 0, 1, 2, 3, or 4. The hydrocarbyl

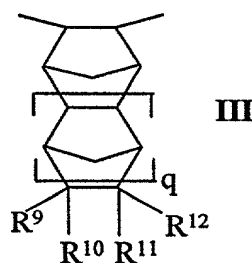
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substituents on the foregoing substituents are composed solely of carbon and hydrogen atoms, such as, for example, branched and unbranched (C_1 to C_{10}) alkyl, branched and unbranched (C_2 to C_{10}) alkenyl, and (C_6 to C_{20}) aryl.

Illustrative examples of hydrocarbyl substituted monomers include

- 5 2-norbornene, 5-methyl-2-norbornene, 5-hexyl-2-norbornene, 5-cyclohexyl-2-norbornene, 5-cyclohexenyl-2-norbornene, 5-butyl-2-norbornene, 5-ethyl-2-norbornene, 5-decyl-2-norbornene, 5-phenyl-2-norbornene, 5-naphthyl-2-norbornene 5-ethylidene-2-norbornene, vinylnorbornene, dicyclopentadiene, dihydrodicyclopentadiene, tetracyclododecene, methyltetracyclododecene, 10 tetracyclododecadiene, dimethyltetracyclododecene, ethyltetracyclododecene, ethylidenyl tetracyclododecene, phenyltetracyclododecene, trimers of cyclopentadiene (e.g., symmetrical and asymmetrical trimers). An especially preferred hydrocarbyl repeating unit is derived from 2-norbornene.

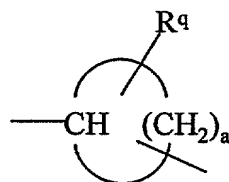
- 15 In another embodiment of the invention, a sacrificial polymer useful in carrying out the invention comprises repeating units represented by Formula III below:



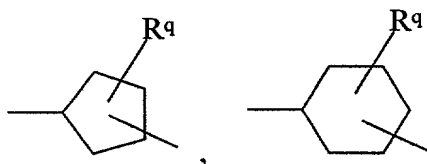
- 20 wherein R^9 to R^{12} independently represent a polar substituent selected from the group: $-(A)_n-C(O)OR''$, $-(A)_n-OR''$, $-(A)_n-OC(O)R''$, $-(A)_n-OC(O)OR''$, $-(A)_n-C(O)R''$, $-(A)_n-OC(O)C(O)OR''$, $-(A)_n-O-A'-C(O)OR''$, $-(A)_n-OC(O)-A'-C(O)OR''$, $-(A)_n-C(O)O-A'-C(O)OR''$, $-(A)_n-C(O)-A'-OR''$, $-(A)_n-C(O)O-A'-OC(O)OR''$, $-(A)_n-C(O)O-A'-O-A'-C(O)OR''$, $-(A)_n-C(O)O-A'-OC(O)C(O)OR''$, $-(A)_n-C(R'')_2CH(R'')(C(O)OR'')$, and $-(A)_n-C(R'')_2CH(C(O)OR'')_2$. The moieties A and A' independently represent a 25 divalent bridging or spacer radical selected from divalent hydrocarbon radicals,

divalent cyclic hydrocarbon radicals, divalent oxygen containing radicals, and divalent cyclic ethers and cyclic diethers, and n is an integer 0 or 1. When n is 0 it should be apparent that A and A' represent a single covalent bond. By divalent is meant that a free valence at each terminal end of the radical are attached to two distinct groups. The divalent hydrocarbon radicals can be represented by the formula $-(C_dH_{2d})-$ where d represents the number of carbon atoms in the alkylene chain and is an integer from 1 to 10. The divalent hydrocarbon radicals are preferably selected from linear and branched (C_1 to C_{10}) alkylene such as methylene, ethylene, propylene, butylene, pentylene, hexylene, heptylene, octylene, nonylene, and decylene. When branched alkylene radicals are contemplated, it is to be understood that a hydrogen atom in the linear alkylene chain is replaced with a linear or branched (C_1 to C_5) alkyl group.

The divalent cyclic hydrocarbon radicals include substituted and unsubstituted (C_3 to C_8) cycloaliphatic moieties represented by the formula:



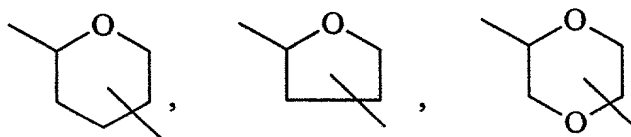
wherein a is an integer from 2 to 7 and R^q when present represents linear and branched (C_1 to C_{10}) alkyl groups. Preferred divalent cycloalkylene radicals include cyclopentylene and cyclohexylene moieties represented by the following structures:



wherein R^q is defined above. As illustrated here and throughout this specification, it is to be understood that the bond lines projecting from the cyclic

structures and/or formulae represent the divalent nature of the moiety and indicate the points at which the carbocyclic atoms are bonded to the adjacent molecular moieties defined in the respective formulae. As is conventional in the art, the diagonal bond line projecting from the center of the cyclic structure indicates that the bond is optionally connected to any one of the carbocyclic atoms in the ring. It is also to be understood that the carbocyclic atom to which the bond line is connected will accommodate one less hydrogen atom to satisfy the valence requirement of carbon.

Preferred divalent cyclic ethers and diethers are represented by the structures:



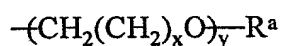
The divalent oxygen containing radicals include (C₂ to C₁₀) alkylene ethers and polyethers. By (C₂ to C₁₀) alkylene ether is meant that the total number of carbon atoms in the divalent ether moiety must at least be 2 and can not exceed 10. The divalent alkylene ethers are represented by the formula -alkylene-O-alkylene- wherein each of the alkylene groups that are bonded to the oxygen atom can be the same or different and are selected from methylene, ethylene, propylene, butylene, pentylene, hexylene, heptylene, octylene, and nonylene. The simplest divalent alkylene ether of the series is the radical -CH₂-O-CH₂-. Preferred polyether moieties include divalent radicals of the formula:



wherein x is an integer from 0 to 5 and y is an integer from 2 to 50 with the proviso that the terminal oxygen atom on the polyether spacer moiety can not be directly linked to a terminal oxygen atom on an adjacent group to form a peroxide linkage. In other words, peroxide linkages (i.e., -O-O-) are not

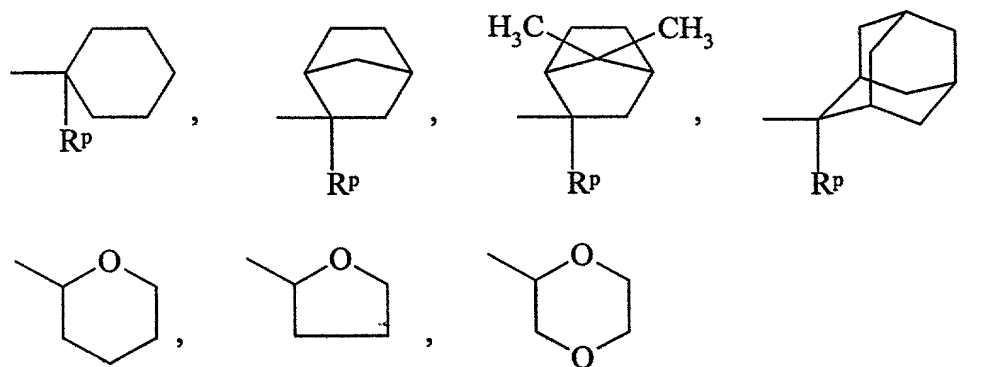
contemplated when polyether spacers are linked to any of the terminal oxygen containing substituent groups set forth under R⁹ to R¹² above.

R⁹ to R¹² can also independently represent hydrogen, linear and branched (C₁ to C₁₀) alkyl, so long as at least one of the remaining R⁹ to R¹² substituents is selected from one of the polar groups represented above. In the formula above p is an integer from 0 to 5 (preferably 0 or 1, more preferably 0). R'' independently represents hydrogen, linear and branched (C₁ to C₁₀) alkyl (e.g., -C(CH₃)), -Si(CH₃), -CH(R^p)OCH₂CH₃, -CH(R^p)OC(CH₃)₃, linear and branched (C₁ to C₁₀) alkoxyalkylene, polyethers, monocyclic and polycyclic (C₄ to C₂₀) cycloaliphatic moieties, cyclic ethers, cyclic ketones, and cyclic esters (lactones). By (C₁ to C₁₀) alkoxyalkylene is meant that a terminal alkyl group is linked through an ether oxygen atom to an alkylene moiety. The radical is a hydrocarbon based ether moiety that can be generically represented as -alkylene-O-alkyl wherein the alkylene and alkyl groups independently contain 1 to 10 carbon atoms each of which can be linear or branched. The polyether radical can be represented by the formula:



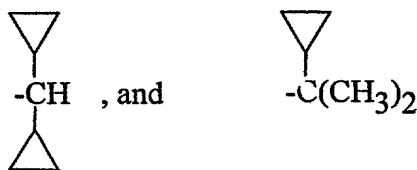
wherein x is an integer from 0 to 5, y is an integer from 2 to 50 and R^a represents hydrogen or linear and branched (C₁ to C₁₀) alkyl. Preferred polyether radicals include poly(ethylene oxide) and poly(propylene oxide). Examples of monocyclic cycloaliphatic monocyclic moieties include cyclopropyl, cyclobutyl, cyclopentyl, cyclohexyl, dicyclopropylmethyl (Dcmp) and dimethylcyclopropylmethyl (Dmcp), and the like. Examples of cycloaliphatic polycyclic moieties include, norbornyl, adamantyl, tetrahydrodicyclopentadienyl (tricyclo[5.2.1.0^{2,6}] decanyl), and the like. Examples of cyclic ethers include tetrahydrofuranyl and tetrahydropyranyl moieties. An example of a cyclic ketone is a 3-oxocyclohexanonyl moiety. An example of a cyclic ester or lactone is a mevalonic lactonyl moiety. Structures for representative cyclic groups set forth above include:

5



10 wherein R^p in the above formulae and cyclic groups represents hydrogen or a linear or branched (C_1 to C_5) alkyl group. The Dcpm and Dmcp substituents are represented, respectively, as follows:

15



The sacrificial polymers useful in practicing the present invention preferably encompass homopolymers and copolymers containing random repeating units derived from a monomer unit or monomer units represented by Formula I, or homopolymers or copolymers containing random repeating units derived from monomer unit or units represented by Formula II, homopolymers or copolymers containing repeating units derived from a monomer unit(s) represented by Formula III and copolymers comprising a combination of repeating units represented by Formulae I and II, Formulae I and III, Formulae II and III or Formulae I, II and III.

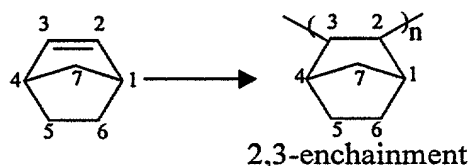
Preferred sacrificial polymers according to the present invention may contain from about 0.1 to 100 mole percent of silyl functional polycyclic repeating units, preferably from about 1 to 50 mole percent, more preferably from about 3 to 25 mole percent, and most preferably from about 5 to 20 mole

percent, with the remainder of the polymer preferably comprising repeating units described under Formula II and/or Formula III. An especially preferred polymer comprises repeating units polymerized from norbornene and triethoxysilylnorbornene in a mole percent ratio of 80/20

5 norbornene/triethoxysilylnorbornene.

Preferred sacrificial polymers according to present invention are addition polymers comprising polycyclic repeating units that are connected to one another via 2,3-linkages that are formed across the double bond contained in the norbornene-type moiety of the prepolymerized polycyclic monomer.

10



15 The polymers may be polymerized from appropriately functionalized norbornene-type monomers in the presence of a single or multi-component Group VIII transition metal catalyst system as described in International Patent Application Publication No. WO 97/20871 to The BFGoodrich Company, published on June 12, 1997, which is hereby incorporated herein by reference in
20 its entirety.

The polynorbornene-type polymer is particularly beneficial because it has a high (>350°C) and sufficient thermal stability to accommodate a number of commonly employed and other semiconductor manufacturing steps such as plasma enhanced chemical vapor deposition (PECVD) of SiO₂ and low
25 temperature copper annealing, and has a decomposition temperature close to its T_g, thereby limiting movement which might damage the semiconductor device.

It is believed that the polycycloolefinic polymers utilized in practicing this invention are uniquely suited as sacrificial materials in semiconductor device
30 manufacturing because the material thermally decomposes close to its T_g. In other words, the polymer remains mechanically stable until the decomposition

temperature is reached enabling the polymer to endure the rather harsh processing steps (e.g., repeated heat cycles) during semiconductor manufacture. The disadvantage with the prior art polymers is that their T_g 's are well below their decomposition temperatures, leading to mechanical failure before the decomposition temperature is reached.

It has been found that by incorporating polycycloolefinic repeating units that contain pendant hydrocarbyl (Formula II) and/or pendant polar (Formula III) substituents into the sacrificial polymer backbone the decomposition temperatures of the silyl substituted polycycloolefinic polymer can be significantly lowered. The decomposition temperature of polymers containing 80/20 mole percent of norbornene/triethoxysilylnorbornene (approximately 430°C) can be lowered by approximately 30°C by replacing the norbornene repeating units in the copolymer with repeating units containing pendant linear and/or branched (C_1 to C_{20}) alkyl substituents. For example, the thermal decomposition temperature for a copolymer containing butylnorbornene/triethoxysilylnorbornene in a mole percent ratio of 95/5 is lowered to 405°C. We expect that the decomposition temperature of the copolymer can be lowered even further (up to approximately 100°C) by replacing the norbornene repeating units in the copolymer with repeating units containing the polar substituents described under Formula III. Homopolymers of norbornyl acetate and norbornyl ethyl carbonate have thermal decomposition temperatures of 356°C and 329°C, respectively. The polar groups include ester, carbonate, and acetate substituents and the like. To effect lower decomposition temperatures of the silyl substituted polymers, the polymer should contain about 50 mole percent of polycyclic repeating units having pendant hydrocarbyl or polar functionality, preferably greater than 50 mole percent, more preferably 51 to 99 percent, still more preferably 55 to 95 mole percent, even more preferably 65 to 80 mole percent.

The aforesaid sacrificial polymers can be heated to above their decomposition temperature, typically in the range of about 380°C to about 450°C, to cause the polymers to decompose into their decomposition products

which can diffuse through various materials used to form semiconductor devices including air gaps. The materials include polymer dielectrics such as silicon dioxide, silicon nitride, silicon oxynitride, and polyimides, for example, Olin-Ciba Geigy (OCG) Probimide® 293 and 412, Amoco Ultradel® 7501 and DuPont
5 Pyralin® 2545. Particularly useful polymers are those that are photosensitive, as will become evident from the following description of other exemplary methods illustrating other aspects of the invention.

The forgoing methodology can be applied to form air gaps in a variety of electrical devices and particularly in relation to electrical interconnects in
10 integrated circuits and other electronic packages. The air gaps may be used on opposite sides of a conductive member or members in both interplanar and intraplanar arrangements to provide a low dielectric insulator with dielectric constants generally less than about 2, more preferably less than about 1.5, still more preferably less than about 1.25 and most preferably about 1.0. The lower
15 the capacitance, the faster the electrical signal can be transmitted through the conductors and the lower the crosstalk between conductors.

As above indicated, the aforesaid polynorbornene polymers are desirable because generally they leave little or essentially no residue. However, some residue may be desirable. For example, a thin film of SiO₂ (or TiO₂ if Ti is used
20 in place of Si in the functionalized norbornene) may be left to insulate the electrical conductors or control corrosion. Actual tests have shown about 100 Å of residue when 5 μm of material are decomposed.

In Figs. 2A-2F, a preferred method of forming an air gap or region between two conductive regions or elements, such as metal lines, is
25 diagrammatically illustrated. In Figs. 2A and 2B, a patterned layer of sacrificial material 30, preferably the aforesaid polynorbornene-type polymer, is formed on a substrate 32. The substrate 32 may have patterns already on it, or it may be an unpatterned material. The substrate may be a base layer or a layer of material overlying a base layer such as an insulating layer of SiO₂ that may
30 overlie the devices on an integrated circuit chip (not shown). By way of specific example, the substrate may be a semiconductor wafer which may, for example,

contain transistors, diodes, and other semiconductor elements (as are well known in the art).

As depicted in Fig. 2A, a uniform layer of the sacrificial material 30 is deposited on the substrate 32. This may be done in any suitable manner, for example, by spin coating, spraying, meniscus, extrusion or other coating methods, by pressing or laying a dry film laminate onto the substrate, etc.

In Fig. 2B, the layer of sacrificial material is patterned to produce the patterned layer of the sacrificial material 30, the pattern of which corresponds to the desired pattern of one or more air gaps to be formed in the semiconductor device. Any suitable technique can be used to pattern the layer of sacrificial material, including, for example, laser ablating, etching, etc. The sacrificial material may be of a type that is or may be made photosensitive to facilitate patterning.

In Fig. 2C, a layer of conductive material 34, particularly metal, is deposited over the patterned layer of sacrificial material 30. This may be done by any suitable technique including, for example, metal sputtering, chemical vapor deposition (CVD), physical vapor deposition (PVD), electroplating, electroless plating, etc.

In Fig. 2D, the metal layer 34 is planarized as needed by any suitable technique including, for example, chemical-mechanical polishing (CMP). If CMP is used with the above-described polynorbornene-type polymer, and other polymers as well, a layer of silicon dioxide preferably is applied to the surface of the sacrificial layer to provide an etch stop.

In Fig. 2E, a permanent dielectric 36 is deposited over the patterned layer of sacrificial material 30 with the metal inlay 34. The permanent dielectric 36 is deposited as a solid layer and covers the sacrificial layer 30 and at least the tops of the metal leads 34. The permanent dielectric layer may be planarized before or after removal of the sacrificial material. The permanent dielectric layer, for example, may be silicon dioxide, polyimide or other material. The permanent dielectric layer may be deposited by spin coating, spray coating or meniscus coating (typically using the sacrificial material dissolved in a solvent), chemical

vapor deposition, plasma enhanced chemical vapor deposition, sol-gel process, or other method. As seen in Fig. 2E, the metal layer can be conveniently formed with a height less than the height of the adjacent sacrificial material. As will be appreciated, this will result in air gaps that extend above the tops of the metal leads, as is desirable to reduce capacitive coupling. Also, the substrate could have trenches formed therein in a pattern corresponding to the pattern of the sacrificial material, so that the resultant air gaps will extend below the metal leads located on lands on the substrate between the trenches.

The sacrificial material 30 is removed through the permanent dielectric layer 36 to form the air gaps 38 as shown in Fig. 2F. The removal of the sacrificial material preferably is accomplished by thermal decomposition and passage of one or more of the decomposition products through the permanent dielectric layer 36 by diffusion. As above indicated, the preferred polynorbornene-type polymer will undergo thermal decomposition at temperatures on the order of about 450°C, and lower, with essentially no residue being left in the air gaps of the resultant semiconductor structure 40. Also, the decomposition products are diffusible through many dielectric materials useful forming the permanent dielectric layer, including in particular polyimides.

The rate of decomposition should be slow enough so that diffusion through the permanent dielectric will occur. Diffusion typically arises from a pressure buildup within the air gap. This pressure build up should not be so great as to exceed the mechanical strength of the permanent dielectric. Increased temperature will generally aid diffusion as diffusivity of gas through the permanent dielectric will normally increase with temperature.

Preferably, the sacrificial material is decomposed at a relatively slow rate. The heating rate preferably is between about 0.5 to 10°C/minute, more preferably between when 1 to 5°C/minute and most preferably about 2 to 3°C/minute.

As will be appreciated, the air gaps may contain residual gas although generally the residual gas will eventually exchange with air. However, steps may

be taken to prevent such exchange, or dispose a different gas (a noble gas for example) or a vacuum in the air gaps. For example, the semiconductor structure may be subjected to vacuum conditions to extract any residual gas from the air gaps by diffusion or other passage through the overcoat layer 24 or
5 otherwise, after which the semiconductor structure may be coated by a suitable sealing material blocking any further passage of gases through the overcoat layer. Before the semiconductor structure is sealed, it may be subjected to a controlled gas atmosphere, such as one containing a noble gas, to fill the air gaps with such gas.

10 As will be appreciated, further processing steps may be performed on the semiconductor structure 40, for example to form additional layer of interconnection in the semiconductor device.

Those skilled in the art will also appreciate that other techniques may be employed to remove the sacrificial material, although less desirable and/or
15 dictated by the type of sacrificial material utilized. The sacrificial material could be a photoresist that will decompose in oxygen (or more generally air or some other oxygen-containing atmosphere, or including an oxygen plasma or ozone). In connection therewith, a permanent layer may comprise, for example, a silica-based xerogel with a 10-90% porosity enabling passage of the oxygen into
20 contact with the photoresist. The oxygen moves through the silica-based xerogel to reach and react with the photoresist to convert it to a gas that passes out through the silica-based xerogel.

In Figs. 3A-3F, a method of forming an air gap or region between two conductive regions or elements, such as metal lines, according to another
25 aspect of the invention, is diagrammatically illustrated. In Figs. 3A and 3B, a patterned layer of conductive material 50, such as aluminum, copper, gold, etc., is formed on a substrate 52. Again, the substrate may be a base layer or a layer of material overlying a base layer such as an insulating layer of SiO₂ that may overlie the devices on an integrated circuit chip (not shown). By way of specific
30 example, the substrate may be a semiconductor wafer which may, for example,

contain transistors, diodes, and other semiconductor elements (as are well known in the art.

As depicted in Fig. 3A, a uniform layer of the conductive material 50 is deposited on the substrate. This may be done in any suitable manner, for example, by metal sputtering, chemical vapor deposition (CVD), plating (particularly electroless plating) or other methods. In Fig. 3B, the layer of conductive material 50 is patterned to produce a pattern of the conductive material corresponding to the desired pattern of one or more electrical conductors, e.g. metal lines, leads, regions, etc., to be formed in the semiconductor device. Any suitable technique can be used to pattern the layer of conductive material, including, for example, laser ablating, etching, etc.

In Fig. 3C, a layer of sacrificial material 54, preferably the aforesaid polynorbornene-type polymer, is deposited over the patterned layer of conductive material 50. This may be done by any suitable technique including, for example, spin coating, spraying, meniscus, extrusion or other coating methods, by pressing or laying a dry film laminate onto the substrate, etc.

In Fig. 3D, any excess sacrificial material overlying the tops of the conductors 50 is removed and the sacrificial layer is planarized, as needed, by any suitable technique including, for example, CMP, reactive ion etching, etc.

In Fig. 3E, a permanent dielectric 56 is deposited over the patterned conductive layer with the sacrificial material inlay. The permanent dielectric is deposited as a solid layer and covers the sacrificial layer and at least the tops of the metal leads of the conductive layer.

Then, like in the manner described above in respect of the method illustrated in Figs. 2A-2F, the sacrificial material is removed through the permanent dielectric layer to form the air gaps 58 as shown in Fig. 3F. Again, the removal of the sacrificial material preferably is accomplished by thermal decomposition and passage of one or more of the decomposition products through the permanent dielectric layer 56 by diffusion. As above indicated, the preferred polynorbornene-type polymer will undergo thermal decomposition at temperatures on the order of about 400°C, and lower, with essentially no

residue being left in the air gaps in the resultant semiconductor structure 60.

Also, the decomposition products are diffusable through many dielectric materials useful in forming the permanent dielectric layer, including in particular polyimides. Also, as above indicated, other techniques may also be employed

5 to remove the sacrificial material, such as the other techniques described above.

Referring now to Figs. 4A-4H, there is illustrated a specific example of a method of forming air gaps (tunnels) in an oxide using the preferred polynorbornene-type polymer. This exemplary specific method involved the steps of:

10 1. In Fig. 4A, a clean, polished silicon wafer 70 is used (although as above indicated many other substrates could be used including ceramic or metal materials).

2. In Fig. 4B, a sacrificial material 72, Avatrel® polynorbornene-type polymer, is spin coated onto the wafer. Spin coating involves rotating the wafer, 15 for example at 1000 to 4000 rotations per minute, and dispensing a solution of the polynorbornene-type polymer and an appropriate solvent in which the polynorbornene-type polymer is dissolved. The solvent may be mesitylene, although other suitable solvents may be used such as decalin or other hydrocarbon solvent. When pendant polar substituents are present on the 20 sacrificial polymer, PGMEA can be employed as a suitable solvent. The spin coating produces a uniform, thin film on the wafer having a thickness of 0.2 to 6 micrometers, with a uniformity less than $\pm 5\%$ across the sample. However, thicker or thinner films could be applied as desired for a given application. After the coating is applied, the wafer is baked in an oven in air at about 100°C to 25 remove the solvent. The polynorbornene-type polymer is then baked at 200 to 300°C in nitrogen for one hour to remove the solvent.

3. In Fig. 4C, a layer of plasma enhanced chemical vapor deposited (PECVD) silicon dioxide 74 is deposited on the surface of the polynorbornene-type polymer 72 using standard conditions. Suitable gases are silane and 30 nitrous oxide.

4. In Fig. 4D, a photoresist 76 is deposited onto the wafer by spin coating, soft baked, exposed, developed and then hard baked under standard conditions following manufacturer's specifications.

5. In Fig. 4E, the sample is reactive ion etched. The pattern in the photoresist 76 is transferred to the silicon dioxide 74 by first using a fluorine containing plasma. The polynorbornene-type polymer 72 is then etched by using an oxygen/fluorine plasma. During the process, the photoresist is also etched. After the polynorbornene-type polymer is etched in the exposed areas, a fluorine plasma is used to strip the silicon dioxide mask. The sample now has only patterned polynorbornene-type polymer 72 as shown in Fig. 4F.

6. In Fig. 4G, silicon dioxide 78 (although other permanent dielectric materials could be used) is deposited onto the patterned polynorbornene-type polymer 72. The process is similar to that used in Step 3 above to deposit the silicon dioxide on the surface of the polynorbornene-type polymer. The polynorbornene-type polymer is now totally encapsulated in a permanent dielectric material 78.

7. In Fig. 4H, the sample is heated to a temperatures greater than the decomposition temperature of the polynorbornene-type polymer 72. The sacrificial material decomposes and one or more of the gaseous decomposition products diffuse out through the overcoat material 78.

8. The result is an oxide composite 80 including air gaps 82 completely surrounded by dielectric material 78.

Referring now to Figs. 5A-5H, there is illustrated a specific example of a method of forming air gaps between metal lines of an electrical interconnect device or layer using the preferred polynorbornene-type polymer. This exemplary specific method involved the steps of:

1. In Fig. 5A, a clean, polished silicon wafer 90 is used.

2. In Fig. 5B, a 1000Å chromium layer followed by 2000Å of gold is sputtered onto the wafer 90 to form a composite chromium/gold layer 92. The sputtering may use direct current (DC) sputtering.

3-7. In Figs. 5C-F, a layer of polynorbornene-type polymer 94 is applied and patterned using silicon dioxide 96 and photoresist 98 as described above in Steps 3-7 of the method illustrated in Figs. 4A-H.

8. In Fig. 5G, the sample is similar to the sample at Step 6 of the method illustrated in Figs. 4A-H, except that a Cr/Au layer 92 lies under the polynorbornene-type polymer 94.

9. In Fig. 5H, gold is plated until its height is the same as the height of the polynorbornene-type polymer 94. The Cr/Au layer 92 serves as an electrical contact and base for the plating of gold between the regions of polynorbornene-type polymer 94. The electroplating may be done in a conventional, pH+7.2 potassium gold cyanide bath using a phosphate buffer.

10. In Fig. 5I, the gold layer 100 and sacrificial layer 94 are coated with PECVD silicon dioxide 102, just as in Step 7 of the method illustrated in Figs. 4A-H.

11. In Fig. 5J, the sample is heated to decompose the polynorbornene-type polymer 94 and form one or more air gaps 104 between adjacent metal lines 100 in the resultant semiconductor structure 106.

Alternative air-gap structures may use various ways of forming the metal pattern so that it is not shorted together. First, electroless plating of metal may replace the electroplating of metal. Second, the metal pattern may be first formed on the silicon wafer (plated to its full height), and then the sacrificial material may be deposited. The sacrificial material covering the metal pattern then may be removed before the permanent dielectric is deposited, as by chemical mechanical polishing, or other techniques.

Reference herein is made to passage of the decomposition products of the sacrificial layer through the permanent layer. This broadly is intended to encompass passage in any manner, including though holes or passages in the permanent layer that may later be closed. However, the preferred mechanism is by diffusion through a solid permanent layer.

Although the invention has been shown and described with respect to a certain preferred embodiment or embodiments, it is obvious that equivalent

alterations and modifications will occur to others skilled in the art upon the reading and understanding of this specification and the annexed drawings. In particular regard to the various functions performed by the above described integers (components, assemblies, devices, compositions, etc.), the terms
5 (including a reference to a "means") used to describe such integers are intended to correspond, unless otherwise indicated, to any integer which performs the specified function of the described integer (i.e., that is functionally equivalent), even though not structurally equivalent to the disclosed structure which performs the function in the herein illustrated exemplary embodiment or embodiments of
10 the invention. In addition, while a particular feature of the invention may have been described above with respect to only one of several illustrated embodiments, such feature may be combined with one or more other features of the other embodiments, as may be desired and advantageous for any given or particular application.

15

What is claimed is:

1. A method of forming an air gap within a semiconductor structure comprising the steps of:

5 using a sacrificial material to occupy a closed interior volume in a semiconductor structure;

causing the sacrificial material to decompose into one or more gaseous decomposition products; and

10 removing at least one of the one or more gaseous decomposition products by passage through at least one solid layer contiguous to the interior volume; and

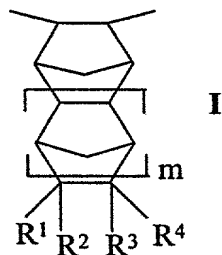
wherein the decomposition of the sacrificial material leaves an air gap at the closed interior volume previously occupied thereby, and the sacrificial material comprises a norbornene-type polymer.

15 2. A method as set forth in claim 1, wherein the at least one solid layer is a dielectric material through which the at least one of the one or more gaseous decomposition products can pass by diffusion under conditions not detrimental to the semiconductor structure.

20 3. A method as set forth in claim 1, wherein the at least one solid layer is a porous dielectric material through which the at least one of the one or more gaseous decomposition products can pass through pores in the porous dielectric material under conditions not detrimental to the semiconductor structure.

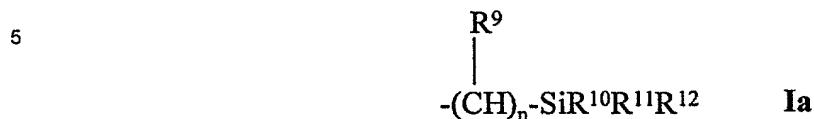
4. A method as set forth in claim 1, wherein said norbornene-type polymer comprises repeat units of the general formula:

25



30

wherein R¹ and R⁴ independently represent hydrogen or linear or branched (C₁ to C₂₀) alkyl; R² and R³ independently represent hydrogen, linear or branched (C₁ to C₂₀) alkyl or the groups:



R⁹ independently is hydrogen, methyl, or ethyl; R¹⁰, R¹¹, and R¹² independently represent linear or branched (C₁ to C₂₀) alkyl, linear or branched (C₁ to C₂₀) alkoxy, linear or branched (C₁ to C₂₀) alkyl carbonyloxy, linear or branched (C₁ to C₂₀) alkyl peroxy, and substituted or unsubstituted (C₆ to C₂₀) aryloxy; m is a number from 0 to 4; and n is a number from 0 to 5; and at least one of substituents R² and R³ is selected from the silyl group represented by the formula set forth under Ia.

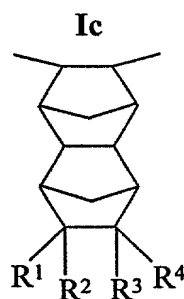
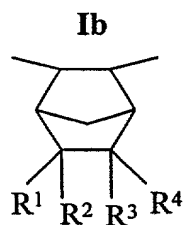
15 5. A method as set forth in claim 4, wherein at least one of R¹⁰, R¹¹, or R¹² is selected from a linear or branched (C₁ to C₁₀) alkoxy group and R⁹ is hydrogen.

20 6. A method as set forth in claim 5, wherein each of R¹⁰, R¹¹, and R¹² are the same and are selected from methoxy, ethoxy, propoxy, butoxy, and pentoxy.

7. A method as set forth in claim 6, wherein n is 0 and R¹⁰, R¹¹, and R¹² are each ethoxy groups.

8. A method as set forth in claim 7, wherein R² or R³ is a triethoxysilyl substituent.

25 9. A method as set forth in claim 1, wherein in Formula I above, m is preferably 0 or 1 as represented by structures Ib and Ic, respectively:

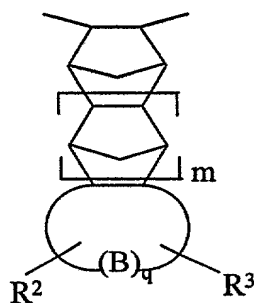


5

wherein R^1 to R^4 are as previously defined and at least one of R^2 and R^3 is a silyl substituent represented by **Ia**.

10

10. A method as set forth in claim 1, wherein R^1 and R^4 taken together with the two ring carbon atoms to which they are attached comprise a repeat unit of the following structure:



15

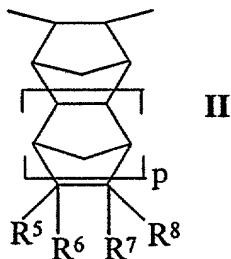
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wherein B is a methylene group, q is a number from 2 to 6, and R^2 and R^3 are as defined above.

11. A method as set forth in claim 1, wherein said norbornene-type polymer further comprises hydrocarbyl substituted polycyclic repeating units selected from units represented by Formula II below:

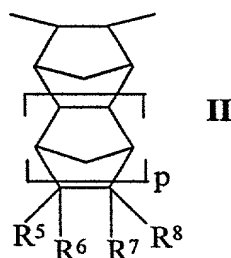
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wherein R^5 , R^6 , R^7 , and R^8 independently represent hydrogen, linear and branched (C_1 to C_{20}) alkyl, hydrocarbyl substituted and unsubstituted (C_5 to C_{12}) cycloalkyl, hydrocarbyl substituted and unsubstituted (C_6 to C_{40}) aryl, hydrocarbyl substituted and unsubstituted (C_7 to C_{15}) aralkyl, (C_3 to C_{20}) alkynyl, linear and branched (C_3 to C_{20}) alkenyl, or vinyl; any of R^5 and R^6 or R^7 and R^8 can be taken together to form a (C_1 to C_{10}) alkylidenyl group, R^5 and R^8 when taken with the two ring carbon atoms to which they are attached can represent saturated and unsaturated cyclic groups containing 4 to 12 carbon atoms or an aromatic ring containing 6 to 17 carbon atoms; and p is 0, 1, 2, 3, or 4.

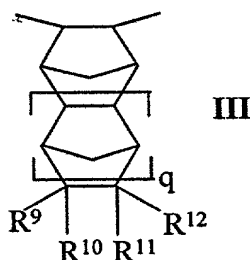
12. A method as set forth in claim 1, wherein said norbornene-type polymer comprises hydrocarbyl substituted polycyclic repeating units selected from units represented by Formula II below:



wherein R^5 , R^6 , R^7 , and R^8 independently represent hydrogen, linear and branched (C_1 to C_{20}) alkyl, hydrocarbyl substituted and unsubstituted (C_5 to C_{12}) cycloalkyl, hydrocarbyl substituted and unsubstituted (C_6 to C_{40}) aryl, hydrocarbyl substituted and unsubstituted (C_7 to C_{15}) aralkyl, (C_3 to C_{20}) alkynyl, linear and branched (C_3 to C_{20}) alkenyl, or vinyl; any of R^5 and R^6 or R^7 and R^8 can be taken

together to form a (C₁ to C₁₀) alkylidenyl group, R⁵ and R⁸ when taken with the two ring carbon atoms to which they are attached can represent saturated and unsaturated cyclic groups containing 4 to 12 carbon atoms or an aromatic ring containing 6 to 17 carbon atoms; and p is 0, 1, 2, 3, or 4.

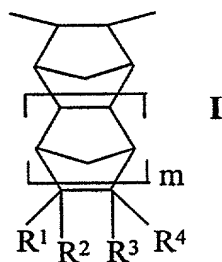
- 5 13. A method as set forth in claim 1, wherein said norbornene-type polymer comprises repeating units represented by Formula III below:



10 wherein R⁹ to R¹² independently represent a polar substituent selected from the group: -(A)_n-C(O)OR'', -(A)_n-OR'', -(A)_n-OC(O)R'', -(A)_n-OC(O)OR'',
 15 -(A)_n-C(O)R'', -(A)_n-OC(O)C(O)OR'', -(A)_n-O-A'-C(O)OR'',
 -(A)_n-OC(O)-A'-C(O)OR'', -(A)_n-C(O)O-A'-C(O)OR'', -(A)_n-C(O)-A'-OR'',
 -(A)_n-C(O)O-A'-OC(O)OR'', -(A)_n-C(O)O-A'-O-A'-C(O)OR'',
 -(A)_n-C(O)O-A'-OC(O)C(O)OR'', -(A)_n-C(R'')₂CH(R'')(C(O)OR''), and
 20 -(A)_n-C(R'')₂CH(C(O)OR'')₂; the moieties A and A' independently represent a divalent bridging or spacer radical selected from divalent hydrocarbon radicals, divalent cyclic hydrocarbon radicals, divalent oxygen containing radicals, and divalent cyclic ethers and cyclic diethers; and r is an integer 0 or 1.

14. A method as set forth in claim 1, wherein said norbornene-type
 25 polymer comprises copolymers comprising a combination of repeating units represented by Formulae I and II, Formulae I and III, Formulae II and III or Formulae I, II and III, where

Formula I is:

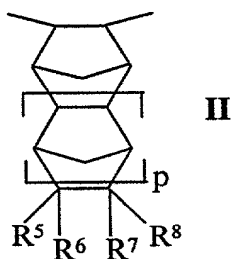


wherein R¹ and R⁴ independently represent hydrogen or linear or branched (C₁ to C₂₀) alkyl; R² and R³ independently represent hydrogen, linear or branched (C₁ to C₂₀) alkyl or the groups:



R⁹ independently is hydrogen, methyl, or ethyl; R¹⁰, R¹¹, and R¹² independently represent linear or branched (C₁ to C₂₀) alkyl, linear or branched (C₁ to C₂₀) alkoxy, linear or branched (C₁ to C₂₀) alkyl carbonyloxy, and substituted or unsubstituted (C₆ to C₂₀) aryloxy; m is a number from 0 to 4; and n is a number from 0 to 5; and at least one of substituents R² and R³ is selected from the silyl group represented by the formula set forth under Ia;

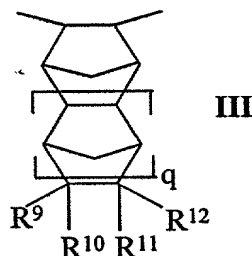
Formula II is



wherein R⁵, R⁶, R⁷, and R⁸ independently represent hydrogen, linear and branched (C₁ to C₂₀) alkyl, hydrocarbyl substituted and unsubstituted (C₅ to C₁₂) cycloalkyl, hydrocarbyl substituted and unsubstituted (C₆ to C₄₀) aryl, hydrocarbyl substituted and unsubstituted (C₇ to C₁₅) aralkyl, (C₃ to C₂₀) alkynyl, linear and

branched (C_3 to C_{20}) alkenyl, or vinyl; any of R^5 and R^6 or R^7 and R^8 can be taken together to form a (C_1 to C_{10}) alkylidenyl group, R^5 and R^8 when taken with the two ring carbon atoms to which they are attached can represent saturated and unsaturated cyclic groups containing 4 to 12 carbon atoms or an aromatic ring containing 6 to 17 carbon atoms; and p is 0, 1, 2, 3, or 4; and

Formula III is



wherein R^9 to R^{12} independently represent a polar substituent selected from the group: $-(A)_n-C(O)OR''$, $-(A)_n-OR''$, $-(A)_n-OC(O)R''$, $-(A)_n-OC(O)OR''$,

$-(A)_n-C(O)R''$, $-(A)_n-OC(O)C(O)OR''$, $-(A)_n-O-A'-C(O)OR''$,
 $-(A)_n-OC(O)-A'-C(O)OR''$, $-(A)_n-C(O)O-A'-C(O)OR''$, $-(A)_n-C(O)-A'-OR''$,
 $-(A)_n-C(O)O-A'-OC(O)OR''$, $-(A)_n-C(O)O-A'-O-A'-C(O)OR''$,
 $-(A)_n-C(O)O-A'-OC(O)C(O)OR''$, $-(A)_n-C(R'')_2CH(R'')(C(O)OR'')$, and
 $-(A)_n-C(R'')_2CH(C(O)OR'')_2$; the moieties A and A' independently represent a

divalent bridging or spacer radical selected from divalent hydrocarbon radicals, divalent cyclic hydrocarbon radicals, divalent oxygen containing radicals, and divalent cyclic ethers and cyclic diethers; and n is an integer 0 or 1.

15. A method as set forth in claim 1, wherein the repeating units containing silyl functional groups comprise at least 1 mole percent of the polymer.

16. A method as set forth in claim 15, wherein the repeating units containing silyl functional groups comprise at least 5 mole percent of the polymer.

17. A method as set forth in claim 1, wherein the sacrificial material is provided in the semiconductor structure by:

forming a patterned layer of the sacrificial material on a substrate corresponding to a pattern of one or more gaps to be formed in the semiconductor structure; and

5 forming another layer of material overlying the patterned layer of sacrificial material.

18. A method as set forth in claim 1, wherein the sacrificial material is provided in the semiconductor structure by:

forming a patterned layer of sacrificial material on a substrate corresponding to a pattern of one or more gaps to be formed in the semiconductor structure;

10 depositing a second material on the substrate within regions bordered by the sacrificial material;

forming another layer of material overlying the patterned layer of sacrificial material and second material in the regions bordered by the sacrificial material.

15 19. A method as set forth in claim 18, wherein the depositing step includes using a conductive material as the second material to form conductive leads.

20 20. A semiconductor device produced in accordance with the method of claim 1.

21. A method of forming one or more air gaps in a semiconductor structure comprising the steps of:

forming a patterned layer of sacrificial material on a substrate corresponding to a pattern of one or more gaps to be formed in the semiconductor structure;

25 depositing a second material on the substrate within regions bordered by the sacrificial material;

forming an overcoat layer of material overlying the patterned layer of sacrificial material and second material in the regions bordered by the sacrificial material;

causing the sacrificial material to decompose into one or more gaseous decomposition products; and

removing at least one of the one or more gaseous decomposition products by passage through the overcoat layer so that one or more air gaps are formed within the semiconductor structure.

22. A method as set forth in claim 21, wherein said depositing of the second material includes using a conductive material to form conductive leads on opposite sides of portions of the sacrificial material.

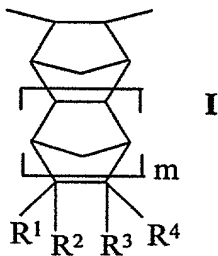
23. A method as set forth in claim 22, wherein prior to said forming of the overcoat layer, the conductive material is formed with a height less than the height of the adjacent sacrificial material.

24. A method as set forth in claim 21, wherein said sacrificial material is a cyclic olefin.

25. A method as set forth in claim 24, wherein said cyclic olefin is a dicyclic olefin.

26. A method as set forth in claim 21, wherein said sacrificial material is a norbornene-type polymer.

27. A method as set forth in claim 26, wherein said norbornene-type polymer comprises repeat units of the general formula:

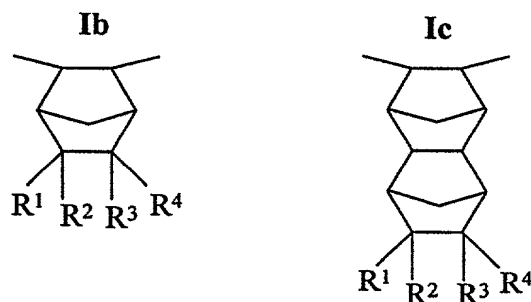


wherein R^1 and R^4 independently represent hydrogen or linear or branched (C_1 to C_{20}) alkyl; R^2 and R^3 independently represent hydrogen, linear or branched (C_1 to C_{20}) alkyl or the groups:



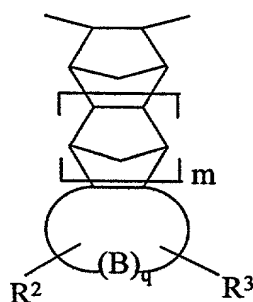
5 R^9 independently is hydrogen, methyl, or ethyl; R^{10} , R^{11} , and R^{12} independently represent linear or branched (C_1 to C_{20}) alkyl, linear or branched (C_1 to C_{20}) alkoxy, linear or branched (C_1 to C_{20}) alkyl carbonyloxy, and substituted or unsubstituted (C_6 to C_{20}) aryloxy; m is a number from 0 to 4; and n is a number from 0 to 5; and at least one of substituents R^2 and R^3 is selected from the silyl group represented by the formula set forth under Ia.

28. A method as set forth in claim 27, wherein in Formula I above, m is preferably 0 or 1 as represented by structures Ib and Ic, respectively:



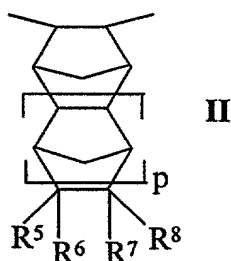
wherein R^1 to R^4 are as previously defined and at least one of R^2 and R^3 is a silyl substituent represented by Ia.

29. A method as set forth in claim 27, wherein R^1 and R^4 taken together with the two ring carbon atoms to which they are attached comprise a repeat unit of the following structure:



5 wherein B is a methylene group, q is a number from 2 to 6, and R² and R³ are as defined above.

30. A method as set forth in claim 27, wherein said norbornene-type polymer further comprises hydrocarbyl substituted polycyclic repeating units selected from units represented by Formula II below:



wherein R⁵, R⁶, R⁷, and R⁸ independently represent hydrogen, linear and branched (C₁ to C₂₀) alkyl, hydrocarbyl substituted and unsubstituted (C₅ to C₁₂) cycloalkyl, hydrocarbyl substituted and unsubstituted (C₆ to C₄₀) aryl, hydrocarbyl substituted and unsubstituted (C₇ to C₁₅) aralkyl, (C₃ to C₂₀) alkynyl, linear and branched (C₃ to C₂₀) alkenyl, or vinyl; any of R⁵ and R⁶ or R⁷ and R⁸ can be taken together to form a (C₁ to C₁₀) alkylidenyl group, R⁵ and R⁸ when taken with the two ring carbon atoms to which they are attached can represent saturated and unsaturated cyclic groups containing 4 to 12 carbon atoms or an aromatic ring containing 6 to 17 carbon atoms; and p is 0, 1, 2, 3, or 4.

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31. A semiconductor device produced in accordance with the method of claim 21.

32. A method of forming an air gap within a structure comprising the steps of:

30 using a sacrificial material to occupy a closed interior volume in the structure;

heating the sacrificial material to cause it to decompose into one or more gaseous decomposition products; and

removing at least one of the one or more gaseous decomposition products by passage through at least one solid layer contiguous to the interior volume; and

wherein the decomposition of the sacrificial material leaves an air gap at the closed interior volume previously occupied by the sacrificial material.

33. A method as set forth in claim 32, wherein the at least one solid layer is a dielectric material through which the at least one of the one or more gaseous decomposition products can pass by diffusion under conditions not detrimental to the semiconductor structure.

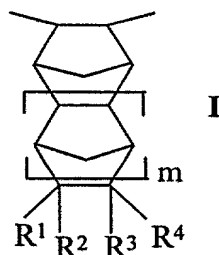
34. A method as set forth in claim 32, wherein the at least one solid layer is a porous dielectric material through which the at least one of the one or more gaseous decomposition products can pass through pores in the porous dielectric material under conditions not detrimental to the semiconductor structure.

35. A method as set forth in claim 32, wherein said sacrificial material is a cyclic olefin.

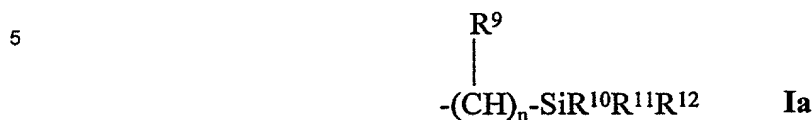
36. A method as set forth in claim 35, wherein said cyclic olefin is a bicycloolefin.

37. A method as set forth in claim 32, wherein said sacrificial material is a norbornene-type polymer.

38. A method as set forth in claim 37, wherein said norbornene-type polymer comprises repeat units of the general formula:

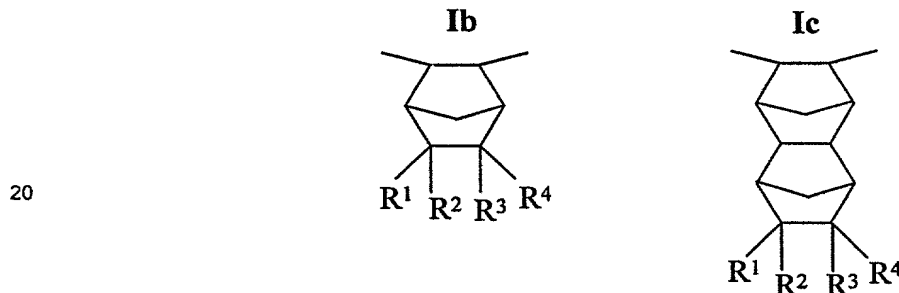


wherein R¹ and R⁴ independently represent hydrogen or linear or branched (C₁ to C₂₀) alkyl; R² and R³ independently represent hydrogen, linear or branched (C₁ to C₂₀) alkyl or the groups:



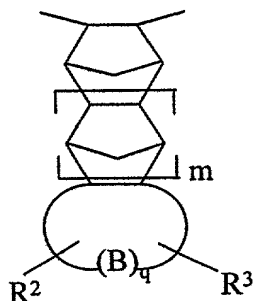
R⁹ independently is hydrogen, methyl, or ethyl; R¹⁰, R¹¹, and R¹² independently represent linear or branched (C₁ to C₂₀) alkyl, linear or branched (C₁ to C₂₀) alkoxy, linear or branched (C₁ to C₂₀) alkyl carbonyloxy, and substituted or unsubstituted (C₆ to C₂₀) aryloxy; m is a number from 0 to 4; and n is a number from 0 to 5; and at least one of substituents R² and R³ is selected from the silyl group represented by the formula set forth under Ia.

39. A method as set forth in claim 38, wherein in Formula I above, m is preferably 0 or 1 as represented by structures Ib and Ic, respectively:



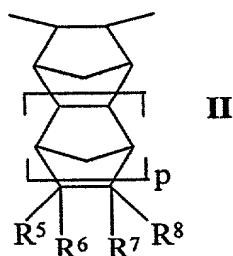
wherein R¹ to R⁴ are as previously defined and at least one of R² and R³ is a silyl substituent represented by Ia.

40. A method as set forth in claim 38, wherein R¹ and R⁴ taken together with the two ring carbon atoms to which they are attached comprise a repeat unit of the following structure:



wherein B is a methylene group, q is a number from 2 to 6, and R² and R³ are as defined above.

41. A method as set forth in claim 38, wherein said norbornene-type polymer further comprises hydrocarbyl substituted polycyclic repeating units selected from units represented by Formula II below:



wherein R⁵, R⁶, R⁷, and R⁸ independently represent hydrogen, linear and branched (C₁ to C₂₀) alkyl, hydrocarbyl substituted and unsubstituted (C₅ to C₁₂) cycloalkyl, hydrocarbyl substituted and unsubstituted (C₆ to C₄₀) aryl, hydrocarbyl substituted and unsubstituted (C₇ to C₁₅) aralkyl, (C₃ to C₂₀) alkynyl, linear and branched (C₃ to C₂₀) alkenyl, or vinyl; any of R⁵ and R⁶ or R⁷ and R⁸ can be taken together to form a (C₁ to C₁₀) alkylidenyl group, R⁵ and R⁸ when taken with the two ring carbon atoms to which they are attached can represent saturated and unsaturated cyclic groups containing 4 to 12 carbon atoms or an aromatic ring containing 6 to 17 carbon atoms; and p is 0, 1, 2, 3, or 4.

42. A semiconductor device produced in accordance with the method of claim 32.

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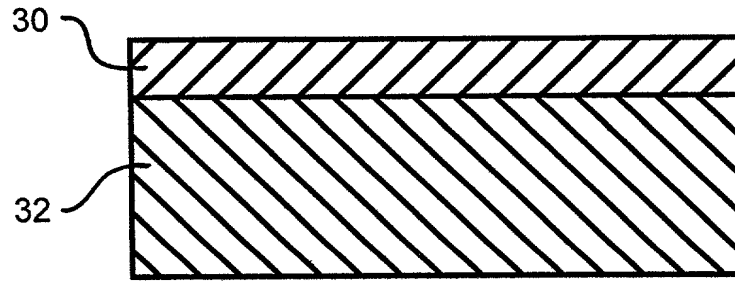


FIG. 2A

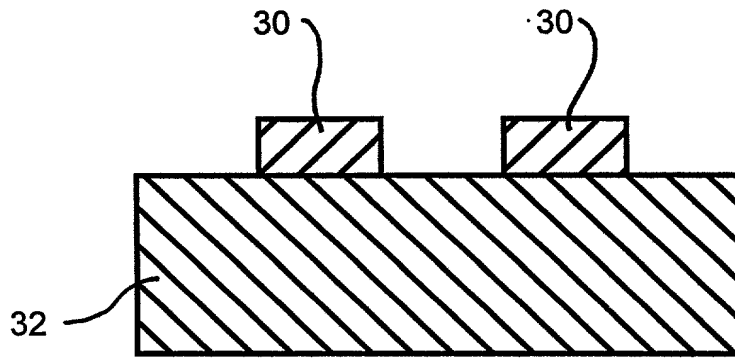


FIG. 2B

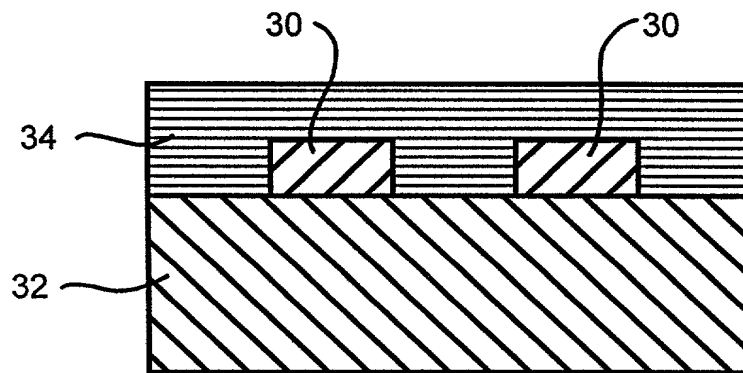


FIG. 2C

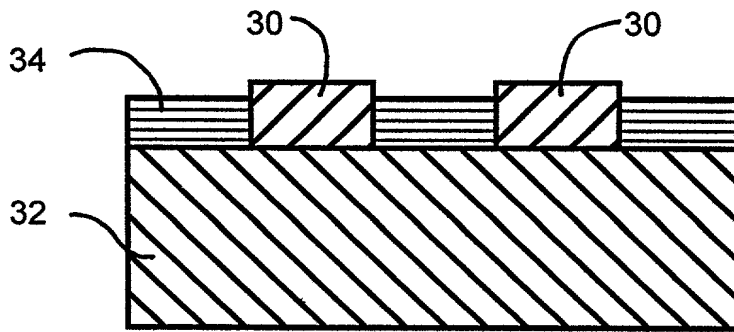


FIG. 2D

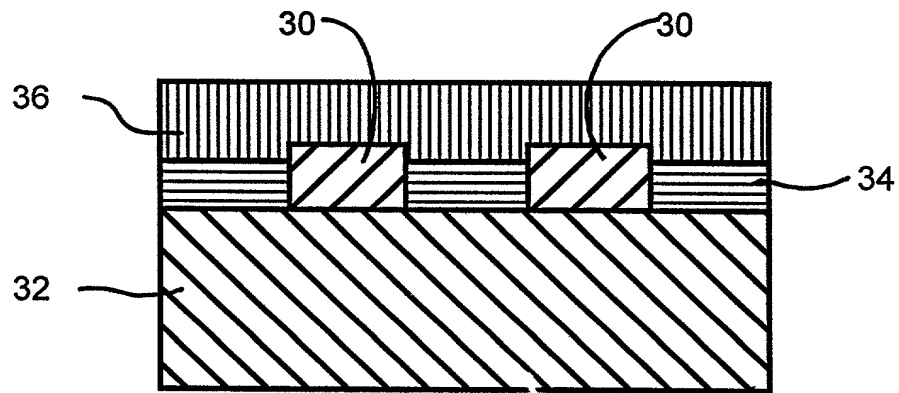


FIG. 2E

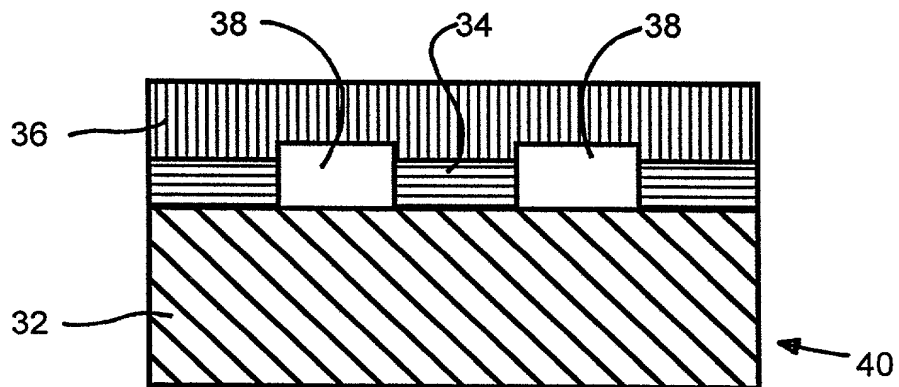


FIG. 2F

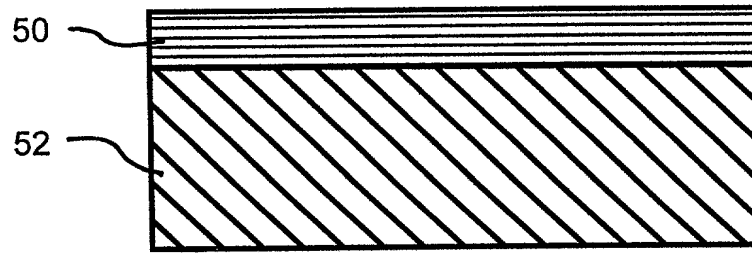


FIG. 3A

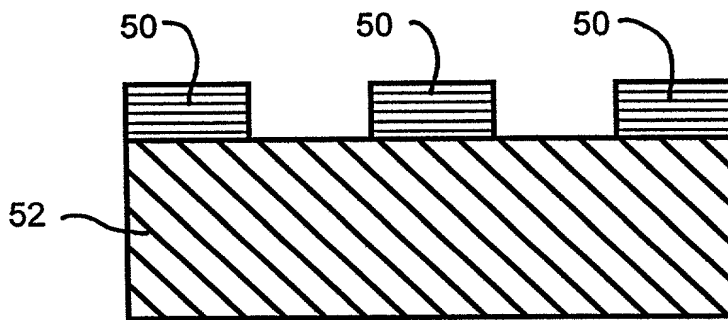


FIG. 3B

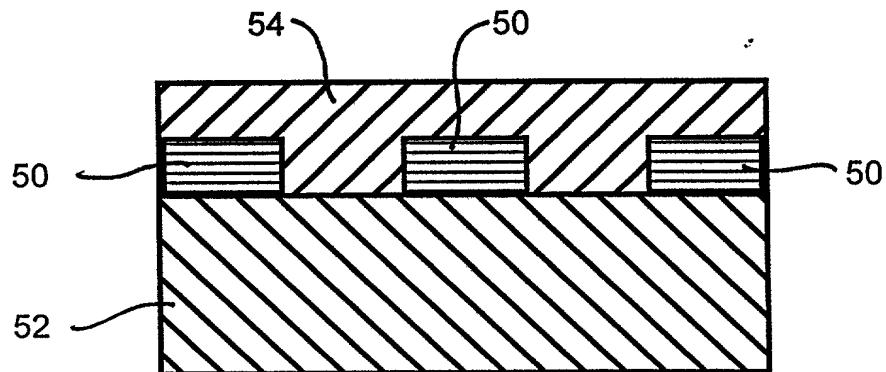


FIG. 3C

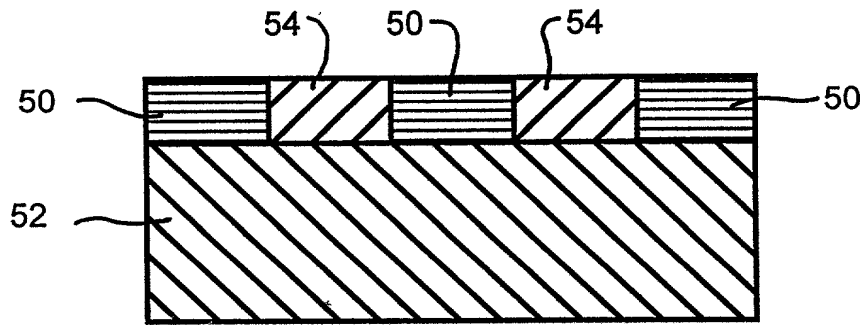


FIG.3D

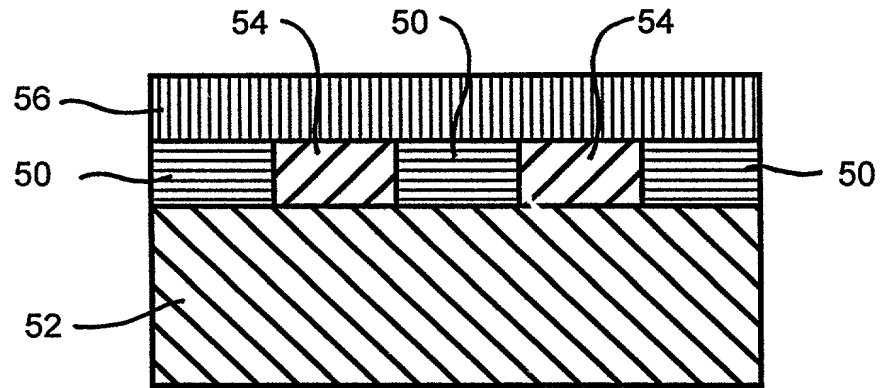


FIG.3E

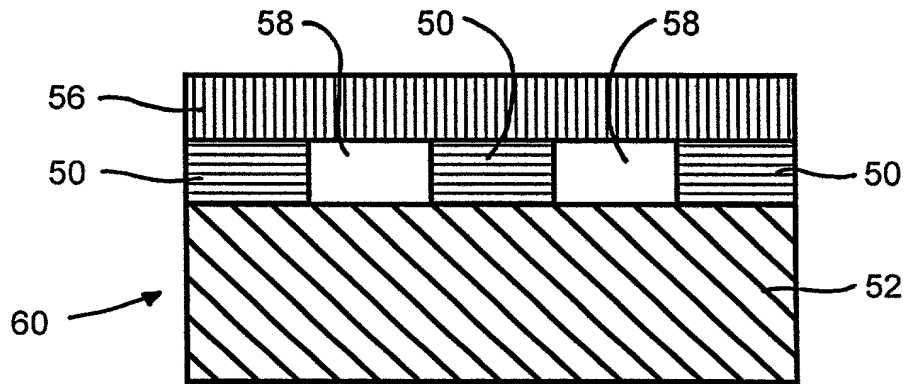


FIG.3F

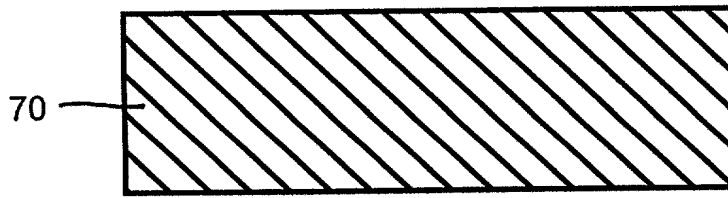


FIG. 4A

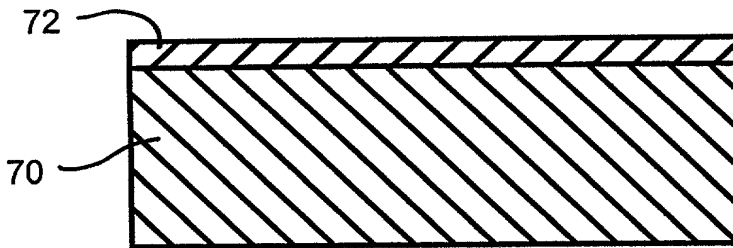


FIG. 4B

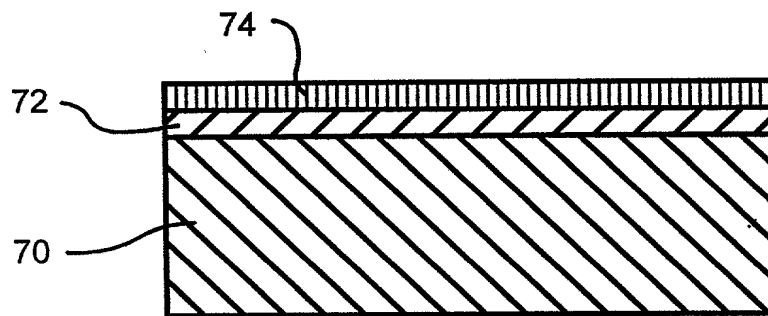


FIG. 4C

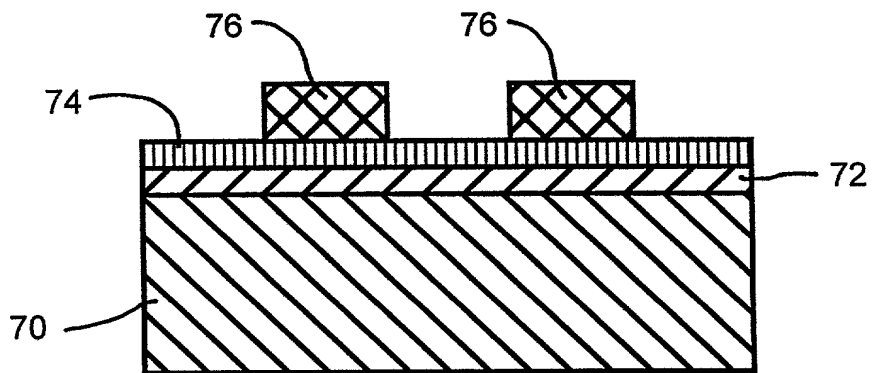


FIG. 4D

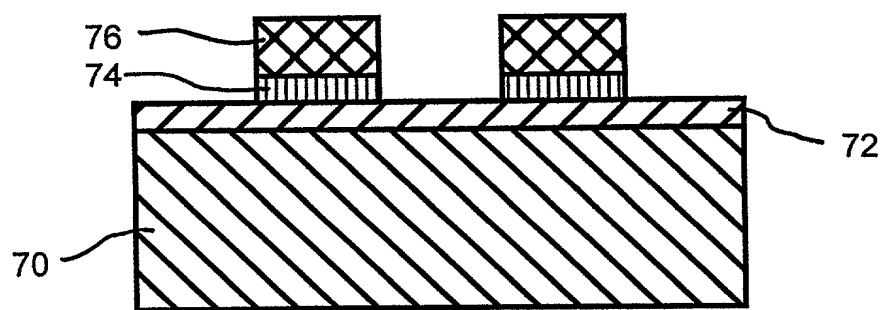


FIG. 4E

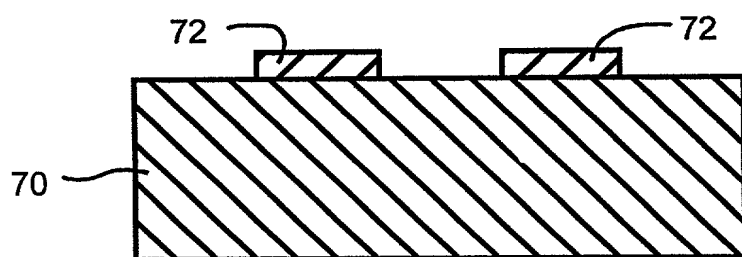


FIG. 4F

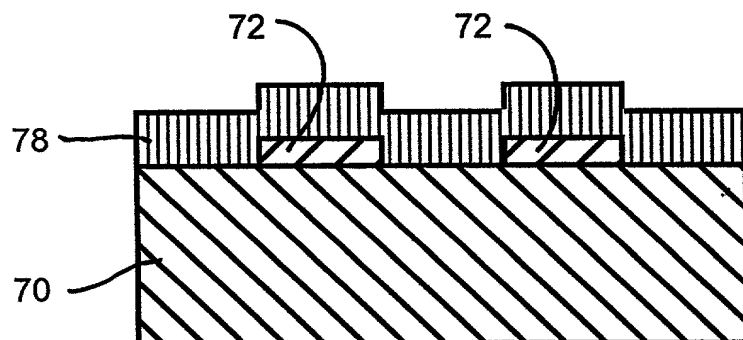


FIG. 4G

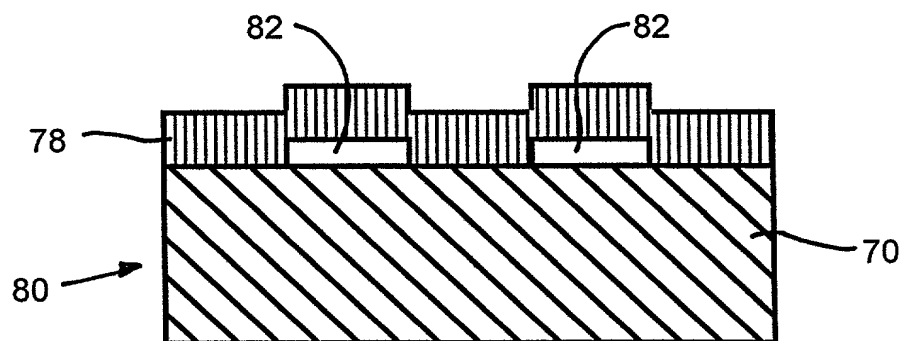


FIG. 4H

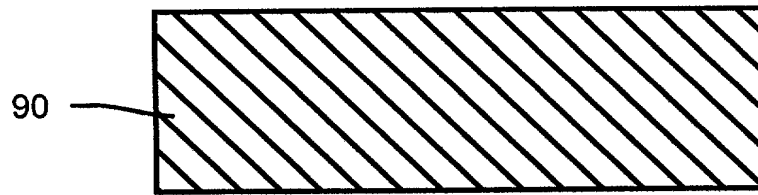


FIG. 5A

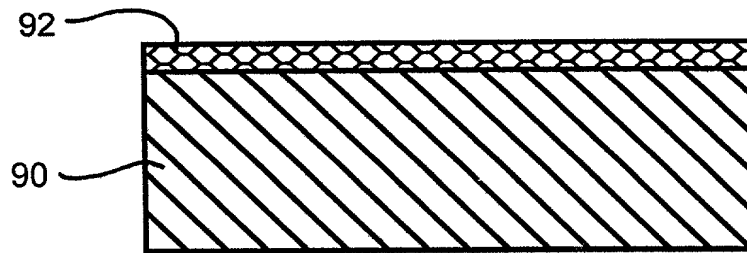


FIG. 5B

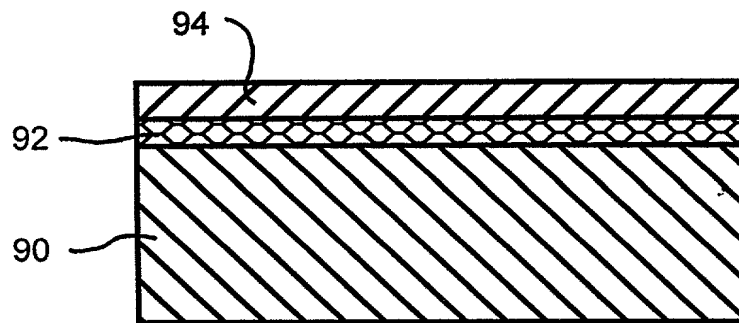


FIG. 5C

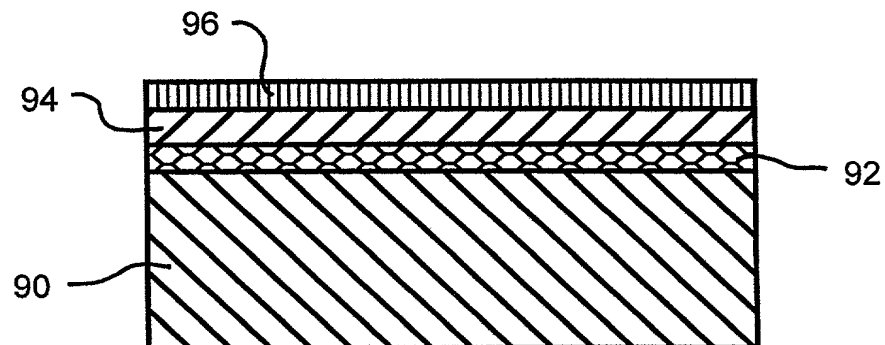


FIG. 5D

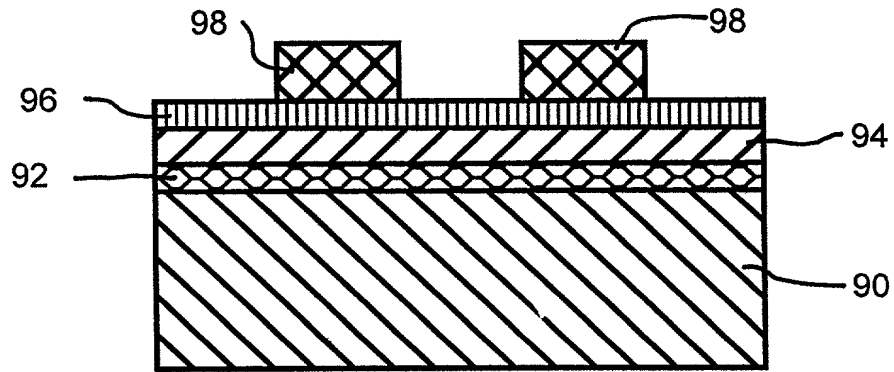


FIG. 5E

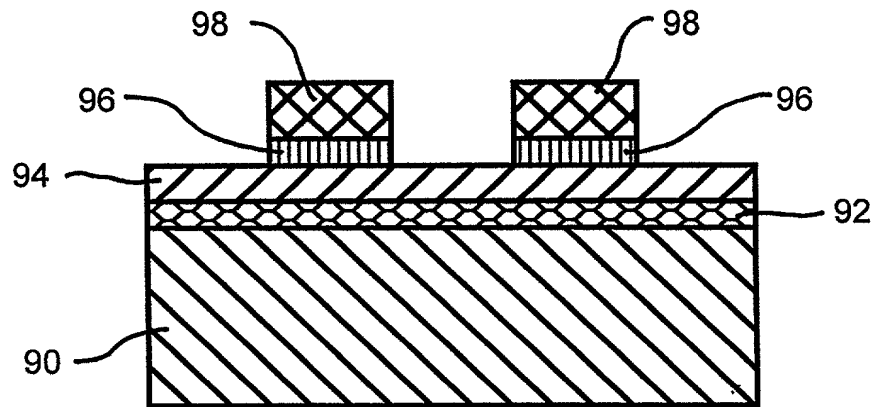


FIG. 5F

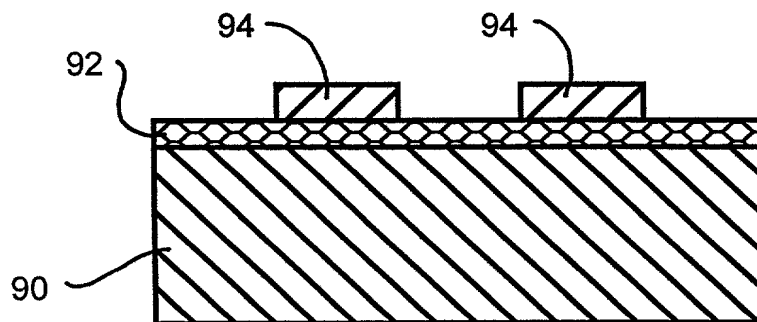


FIG. 5G

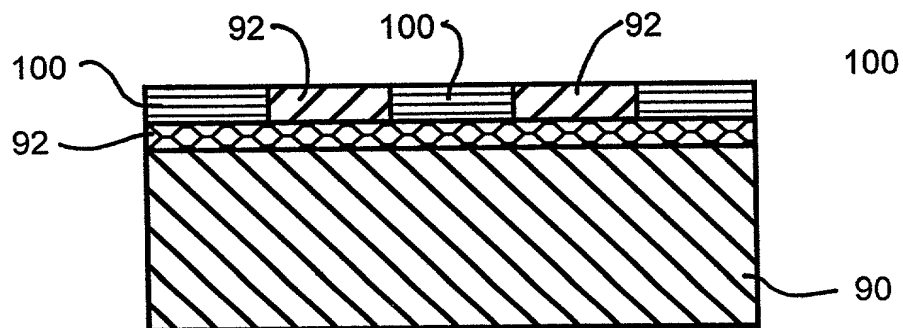


FIG. 5H

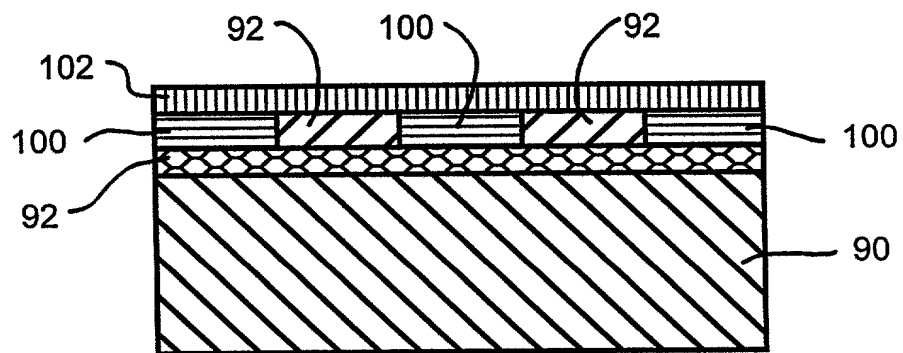


FIG. 5I

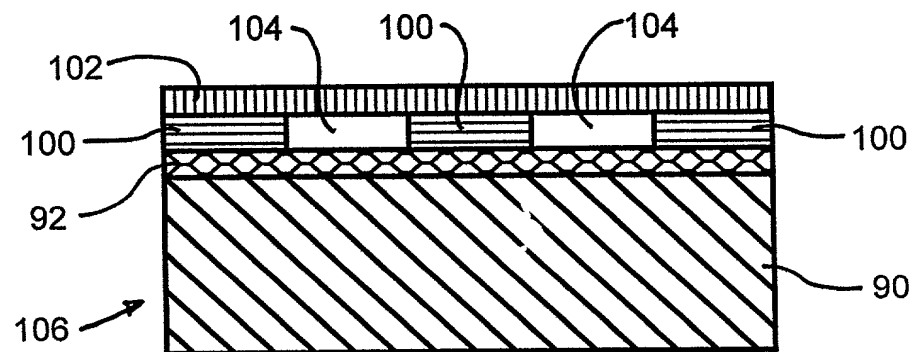


FIG. 5J

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name, and I verily believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed for which a patent is sought on the invention entitled:

Fabrication of a Semiconductor Device with Air Gaps for Ultra-Low Capacitance Interconnections
the specification of which was filed on January 21, 1998 as Application Serial No. 09/009,952.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the patentability and the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby claim the benefit under Title 35, United States Code, Section 120/365 of all United States and PCT international application(s) listed below and insofar as the subject matter of each of the claims of this application is not disclosed in such prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56(a), which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

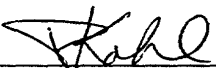
60/035,848
(Application Serial No.)

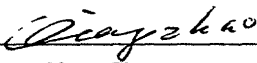
January 21, 1997
(Filing Date)

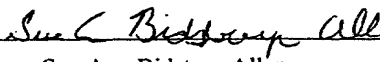
abandoned
(Status - patented,
pending, abandoned)

I hereby appoint as my attorneys and/or agent(s) Mary Ann Tucker, Reg. No. 27,081; Brian M. Kolkowski, Reg. No. 36,847; Thoburn T. Dunlap, Reg. No. 31,246; George W. Moxon II, Reg. No. 26,615; and Don W. Bulson, Reg. No. 28,192; with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith. Address all telephone calls to Thoburn T. Dunlap at telephone number 216-447-5918. Please mail all correspondence to The B.F. Goodrich Company, Patent Law Department, 9921 Brecksville Road, Brecksville, Ohio 44141-3289.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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